Fast verified post-quantum software

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Houston, we have a problem ...

My talk at ICMC 2019: "Does open-source cryptographic software work correctly?"

Talk right now in ICMC 2021 track 2: "Overview of open-source cryptography vulnerabilities."

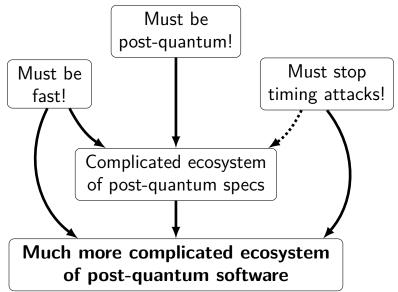
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2021.07 Blessing—Specter—Weitzner "You really shouldn't roll your own crypto: an empirical study of vulnerabilities in cryptographic libraries": 73 "actual" cryptographic vulnerabilities, including 11 "severe" cryptographic vulnerabilities, among OpenSSL, GnuTLS, Mozilla TLS, WolfSSL, Botan, Libgcrypt, LibreSSL, BoringSSL post-2010 CVEs.

... and the complexity is getting worse



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Symbolic-testing tools check that optimized software equals reference software. "Equals": gives the same outputs **for all inputs**.

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This talk: new saferewrite symbolic-testing tool. Open source from https://pqsrc.cr.yp.to.

Under the hood, doing most of the work: valgrind; its VEX library; Z3 theorem prover; angr.io binary-analysis/symbolic-execution toolkit.

Case study: int16[64] comparison

Subroutine used inside Frodo post-quantum KEM. My ref version, cmp_64xint16/ref/verify.c:

```
#include <stdint.h>
int cmp 64xint16(const uint16 t *x,
                  const uint16 t *y)
{ for (int i = 0; i < 64; ++i)
    if (x[i] != y[i])
      return -1;
  return 0;
```

Automatic saferewrite analysis

Using clang -01 -fwrapv -march=native:

- saferewrite says unsafe-valgrindfailure: Code has variable branches/indices, violating constant-time coding discipline.
- And unsafe-unrollsplit-65:
 Unrolling split the code into 65 cases.

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- And unsafe-unrollsplit-65: Unrolling split the code into 65 cases.

Using gcc -03 -march=native -mtune=native:

- unsafe-valgrindfailure
- unsafe-unrollsplit-65
- equals-ref-clang_-01_...: cmp_64xint16 binaries give same outputs.

Automatic analysis of a rewrite

```
#include <stdint.h>
#include <string.h>
int cmp 64xint16(const uint16 t *x,
                 const uint16 t *y)
  return memcmp(x,y,128);
```

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#include <stdint.h>
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```

Again unsafe-valgrindfailure: variable time. Also unsafe-differentfrom-ref-clang_.... Why? Nonzero memcmp output isn't always -1.

Automatic analysis of another rewrite

```
#include <stdint.h>
#include <string.h>
int cmp 64xint16(const uint16 t *x,
                 const uint16 t *y)
{ int r = memcmp(x,y,128);
  if (r != 0) return -1;
  return 0;
```

Automatic analysis of another rewrite

```
#include <stdint.h>
#include <string.h>
int cmp 64xint16(const uint16 t *x,
                 const uint16 t *y)
{ int r = memcmp(x,y,128);
  if (r != 0) return -1;
  return 0;
```

Now equals-ref-clang_... but still unsafe-valgrindfailure. 2017 Frodo software used memcmp; broken by 2020.06 timing attack.

2020.06 Frodo official constant-time code

```
int8_t ct_verify(const uint16 t *a,
              const uint16 t *b, size t len)
{ // Compare two arrays in constant time.
  // Returns 0 if the byte arrays are equal,
  // -1 otherwise.
    uint16 t r = 0;
    for (size t i = 0; i < len; i++) {
        r \mid = a[i] \hat{b}[i]:
    r=(-(int16 t)r)>(8*sizeof(uint16 t)-1);
    return (int8 t)r;
```

Use saferewrite to analyze this ...

Add wrapper to fit the cmp_64xint16 interface:

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Feed ct_verify and wrapper to saferewrite:

No more unsafe-valgrindfailure: Great.

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Feed ct_verify and wrapper to saferewrite:

- No more unsafe-valgrindfailure: Great.
- unsafe-differentfrom-ref-...: Oops!

Bug discovered 2020.12 by Saarinen; easy to exploit.

A safe rewrite: correct constant-time code

```
#include <stdint.h>
int cmp 64xint16(const uint16 t *x,
                 const uint16 t *y)
{ uint32 t differences = 0;
  for (long long i = 0; i < 64; ++i)
    differences |= x[i] ^ y[i];
  return (1 & ((differences - 1) >> 16)) - 1;
```

Now saferewrite analysis with both compilers says equals-ref-... and no more unsafe.

10 sample implementations of cmp_64xint16. One uses OpenSSL's CRYPTO_memcmp Intel asm; see CVE-2018-0733 re CRYPTO memcmp HP asm.

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Analysis of everything (multicore) done in 8 mins.

Laptop tip: chmod +t src/*; chmod -t src/cmp*

Example: integer-sequence encoders

Existing optimized code from NTRU Prime, with heavy use of Intel AVX2 vector instructions:

• 245-line encode_761x1531/avx/encode.c encode.c and similar encoders for other sizes are automatically generated by 239-line Python script.

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Existing reference code, much simpler:

- 38-line encode_761x1531/ref/Encode.c
- 18-line encode_761x1531/ref/wrapper.c

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Existing reference code, much simpler:

- 38-line encode_761x1531/ref/Encode.c
- 18-line encode_761x1531/ref/wrapper.c "Is the optimized code a safe rewrite of ref?" Automatic saferewrite analysis: equals-ref.

Excerpt from avx/encode.c

```
x = mm256 loadu si256((m256i *) reading);
x = _{mm256\_add\_epi16(x,_{mm256\_set1\_epi16(2295))};
x &= _mm256_set1_epi16(16383);
x = _{mm256\_mulhi\_epi16(x,_{mm256\_set1\_epi16(21846))};
y = x \& _mm256_set1_epi32(65535);
x = mm256 srli epi32(x,16);
x = mm256 \text{ mullo epi32}(x, mm256 \text{ set1 epi32}(1531));
x = mm256 \text{ add epi}32(y,x);
x = mm256 \text{ shuffle epi8}(x, mm256 \text{ set epi8}(
  12,8,4,0,12,8,4,0,14,13,10,9,6,5,2,1,
  12,8,4,0,12,8,4,0,14,13,10,9,6,5,2,1
  )):
x = mm256 permute4x64 epi64(x,0xd8);
mm storeu si128(( m128i *) writing,
                  _mm256_extractf128_si256(x,0));
*((uint32 *) (out+0)) = _mm256_extract_epi32(x,4);
*((uint32 *) (out+4)) = mm256 extract epi32(x,6);
```

More subroutines in NTRU Prime code

```
equals; total core-minutes
                    avx=int16=p=ref; 38 min
decode 761x1531:
                             avx=ref; 0.3 min
decode 761x3:
decode 761x4591:
                    avx=int16=p=ref; 39 min
decode 761xint16:
                          little=ref; 0.3 min
decode 761xint32:
                          little=ref; 0.3 min
encode 761x1531:
                   avx=portable=ref; 17 min
encode 761x1531round:
                               avx=ref; 6 min
                             avx=ref; 0.4 min
encode 761x3:
encode 761x4591:
                    avx=portable=ref; 6 min
encode 761xfreeze3:
                      missing asm insn in angr!
encode 761xint16:
                          little=ref: 0.4 min
```

Active, responsive angr development team

Actions

Projects 3

Security

Reviewers

Assignees

Lahels

None vet

Projects

None yet

No one assigned

Insights



Issues 542

other-sized variants.

The answer is obvious! "no."

<> Code

ໃ Pull requests 71

ltfish merged 1 commit into master from fix/signed saturation packing □ 4 hours ago **№** Meraed Conversation 2 -O- Commits 1 F Checks 13 ± Files changed 2 rhelmot commented 8 hours ago Member No reviews As per dib's email. This addresses the issue with vpackuswb (van I'm really curious what the

fuck you were thinking when you wrote this code 4 years ago) but I'm still looking into the

ltfish commented 8 hours ago Member was this code ever tested?

Other subroutines in NTRU Prime code

```
\begin{array}{lll} \texttt{decode\_256x2:} & \texttt{avx=ref; 0.3 min} \\ \texttt{encode\_256x2:} & \texttt{avx=ref; 0.2 min} \\ \texttt{core\_scale3sntrup761:} & \texttt{avx=ref; 11 min} \\ \texttt{core\_weightsntrup761:} & \texttt{avx=ref; 10 min} \\ \texttt{core\_wforcesntrup761:} & \texttt{avx=ref=r2=s; 31 min} \\ \end{array}
```

Not integrated into saferewrite yet:

- core_inv3sntrup761: avx vs. ref
- core_invsntrup761: avx vs. ref
- core_mult3sntrup761: avx vs. 32 vs. ref
- core_multsntrup761: avx vs. ref

Status: Multiplication software is partially verified.

Links, TODO

#saferewrite

saferewrite package is available now from https://pqsrc.cr.yp.to. Work in progress:

- More post-quantum case studies.
- More pre-quantum case studies: e.g., Ed25519.
- More languages: e.g., support Python ref.
- Developer integration: incremental testing etc.
- "Cuts": subroutine swaps etc. for faster testing.
- Plugins for dedicated equivalence testers.
- Higher assurance for the entire toolchain.

Related work: Cryptol/SAW/hacrypto, Cryptoline, Fiat-Crypto, HACL*, Jasmin, ValeCrypt, VST.