

Can cryptographic software
be fixed?

D. J. Bernstein

Bob's laptop screen:

From: Alice

Thank you for your
submission. We received
many interesting papers,
and unfortunately your

Bob assumes this message is
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But today's "security" systems
fail to guarantee this property.
Attacker could have modified
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Any hope of fixing this?

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Focus of this talk:

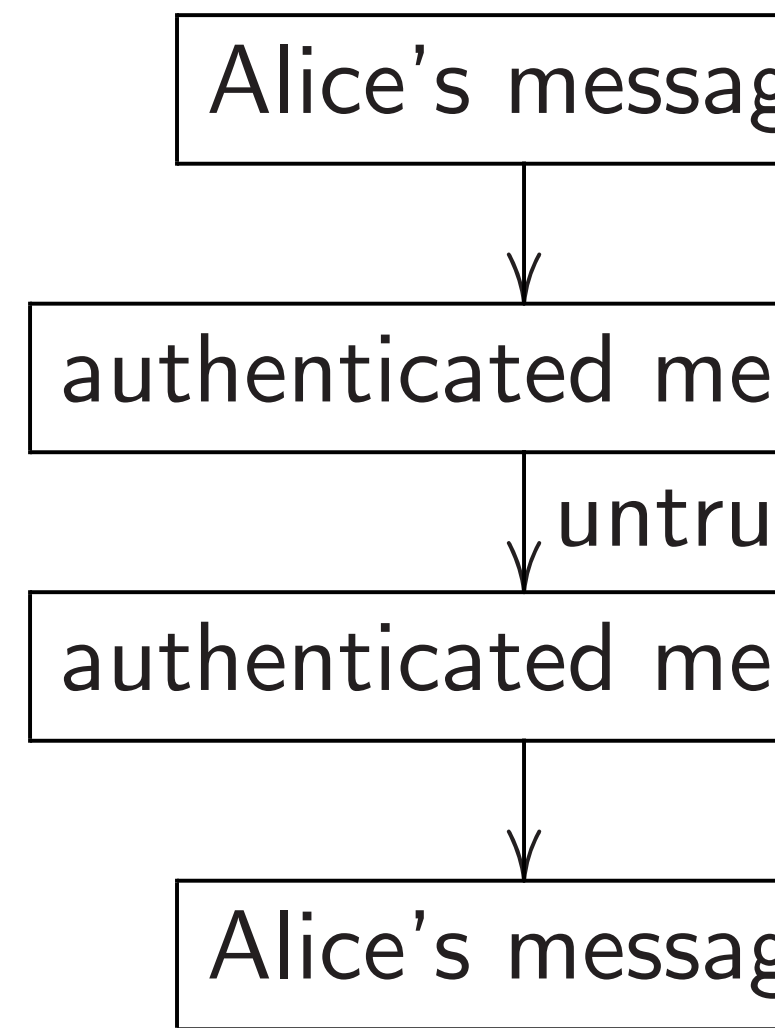
How does Bob's laptop

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Cryptographic solutions

Message-authentication



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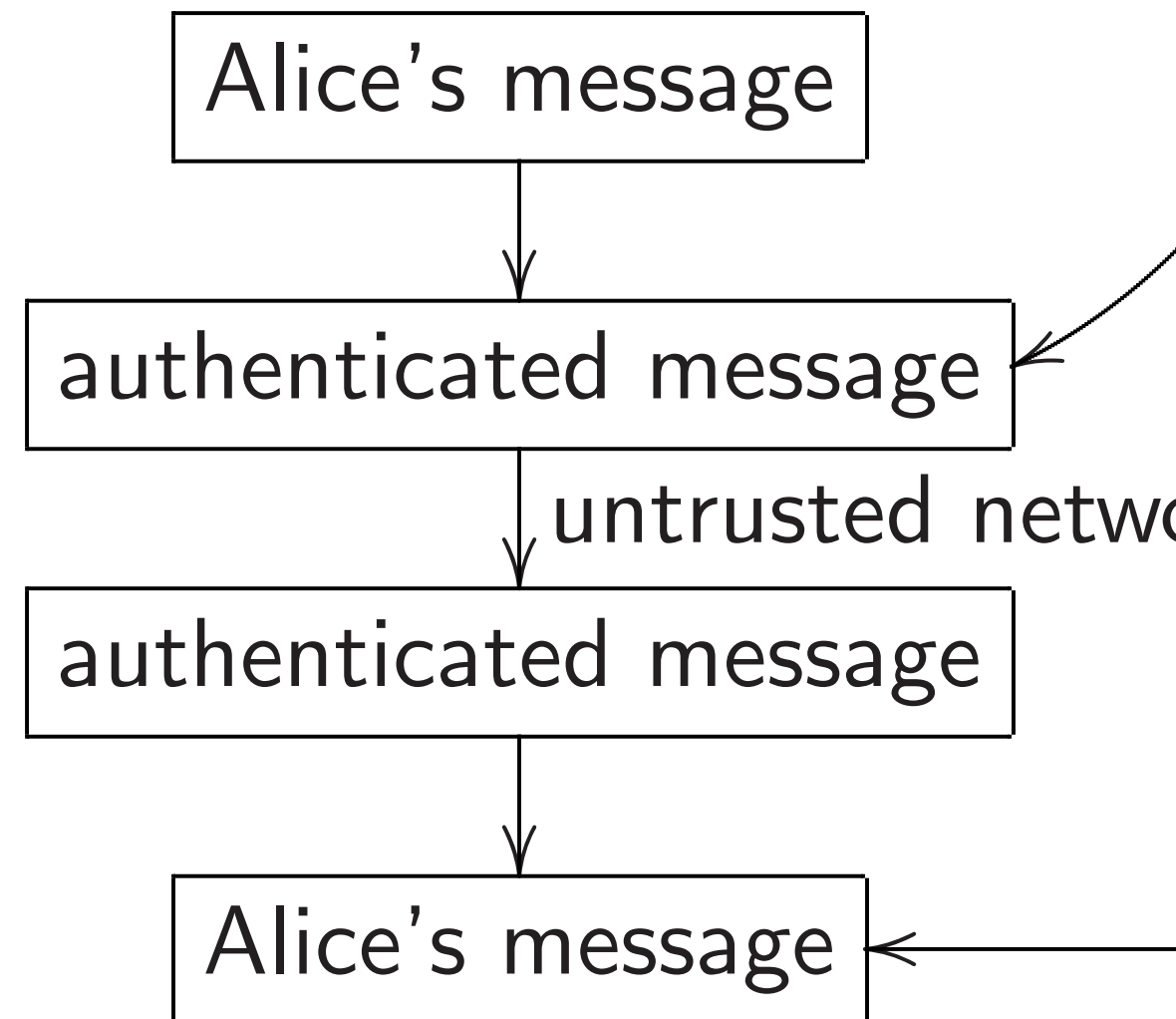
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Focus of this talk: Cryptography

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Cryptographic solution: Message-authentication code



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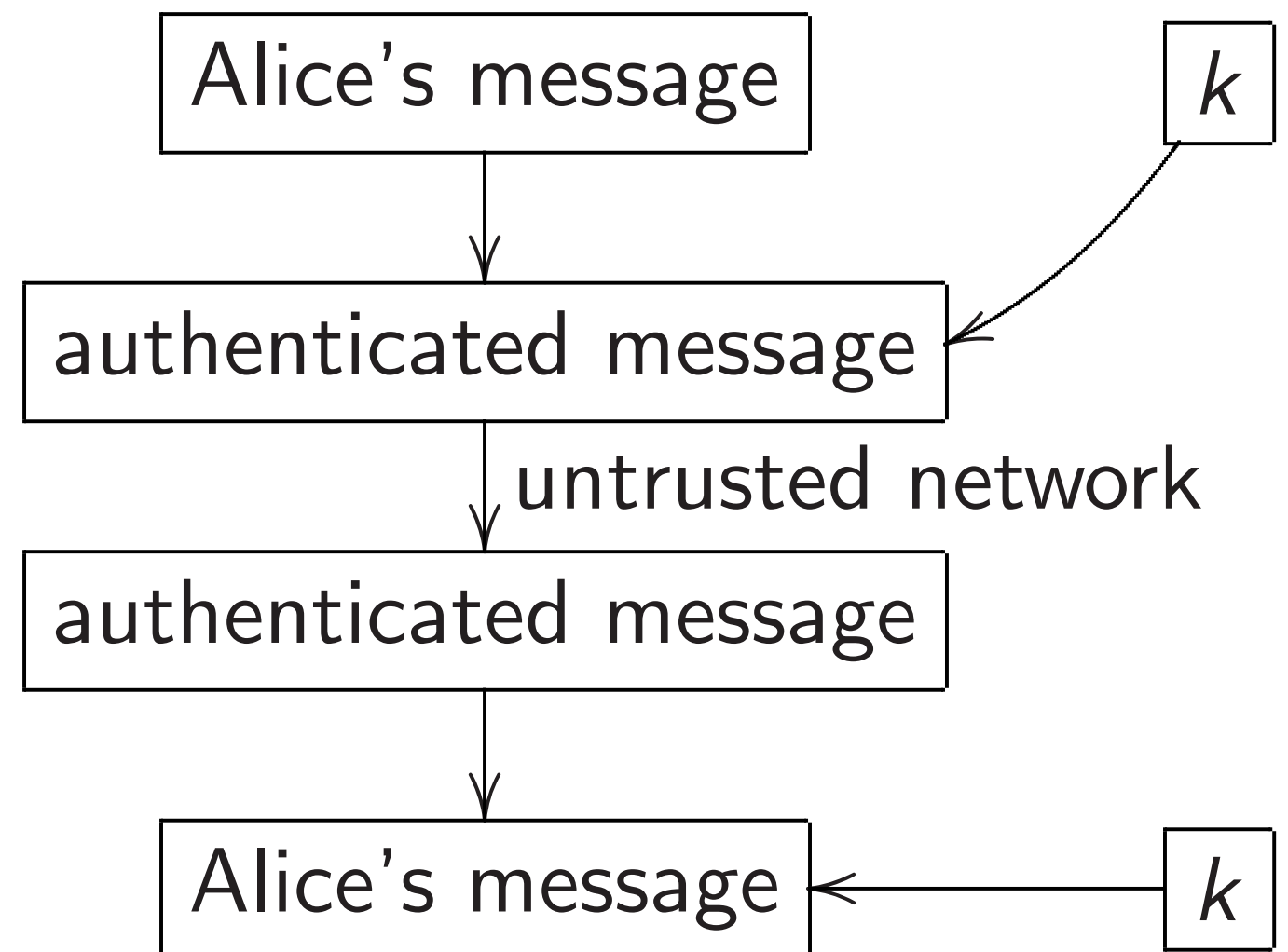
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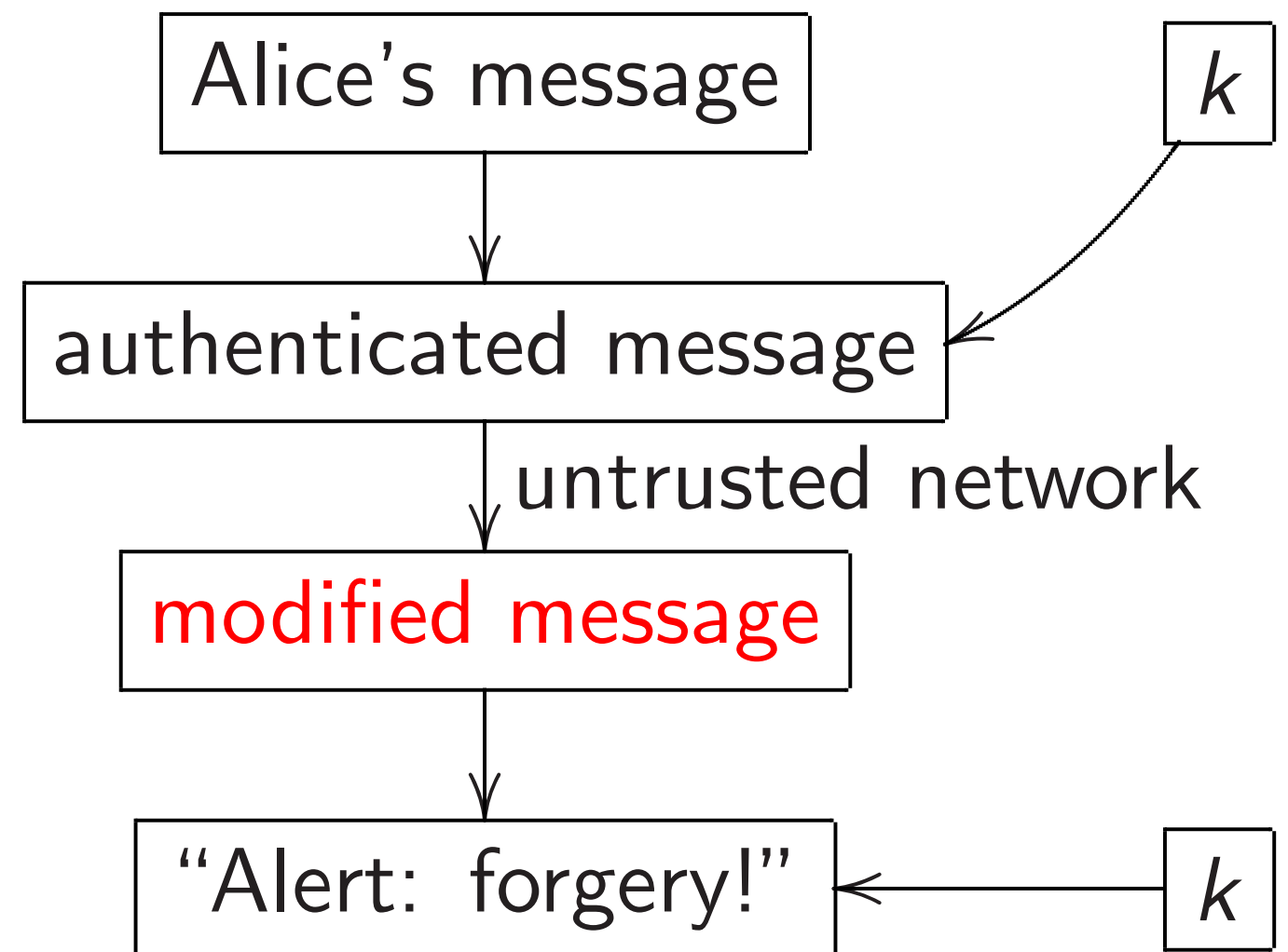
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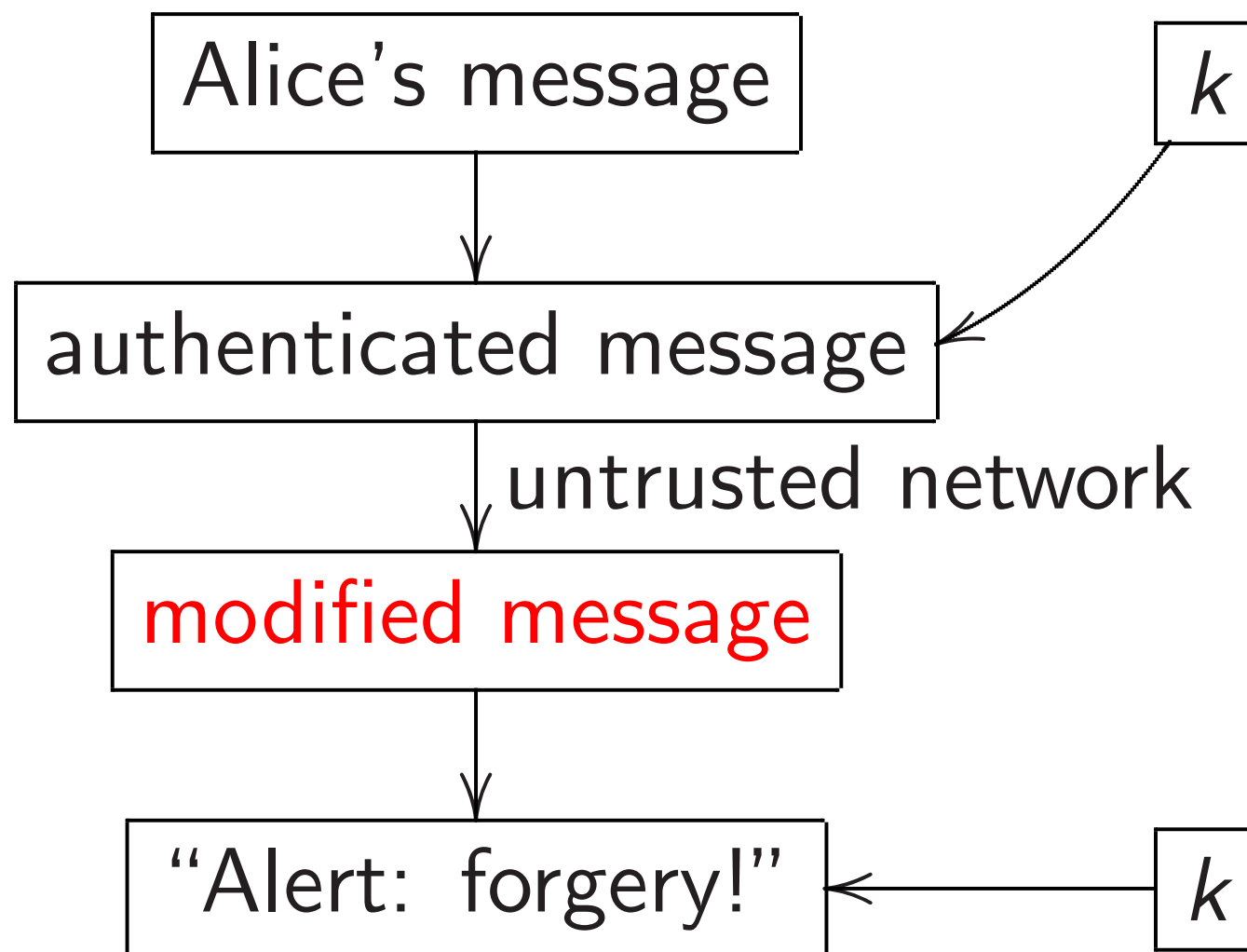
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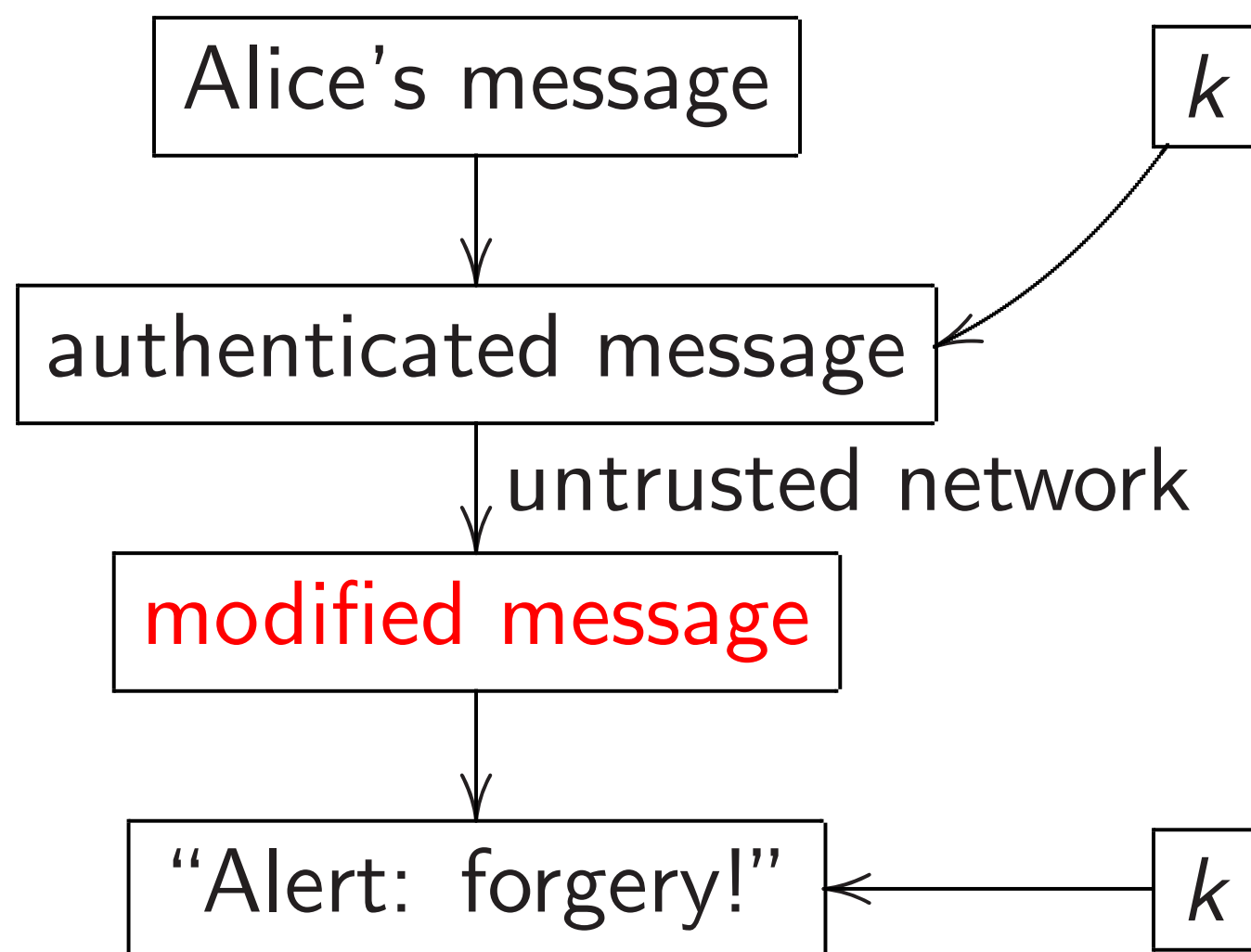
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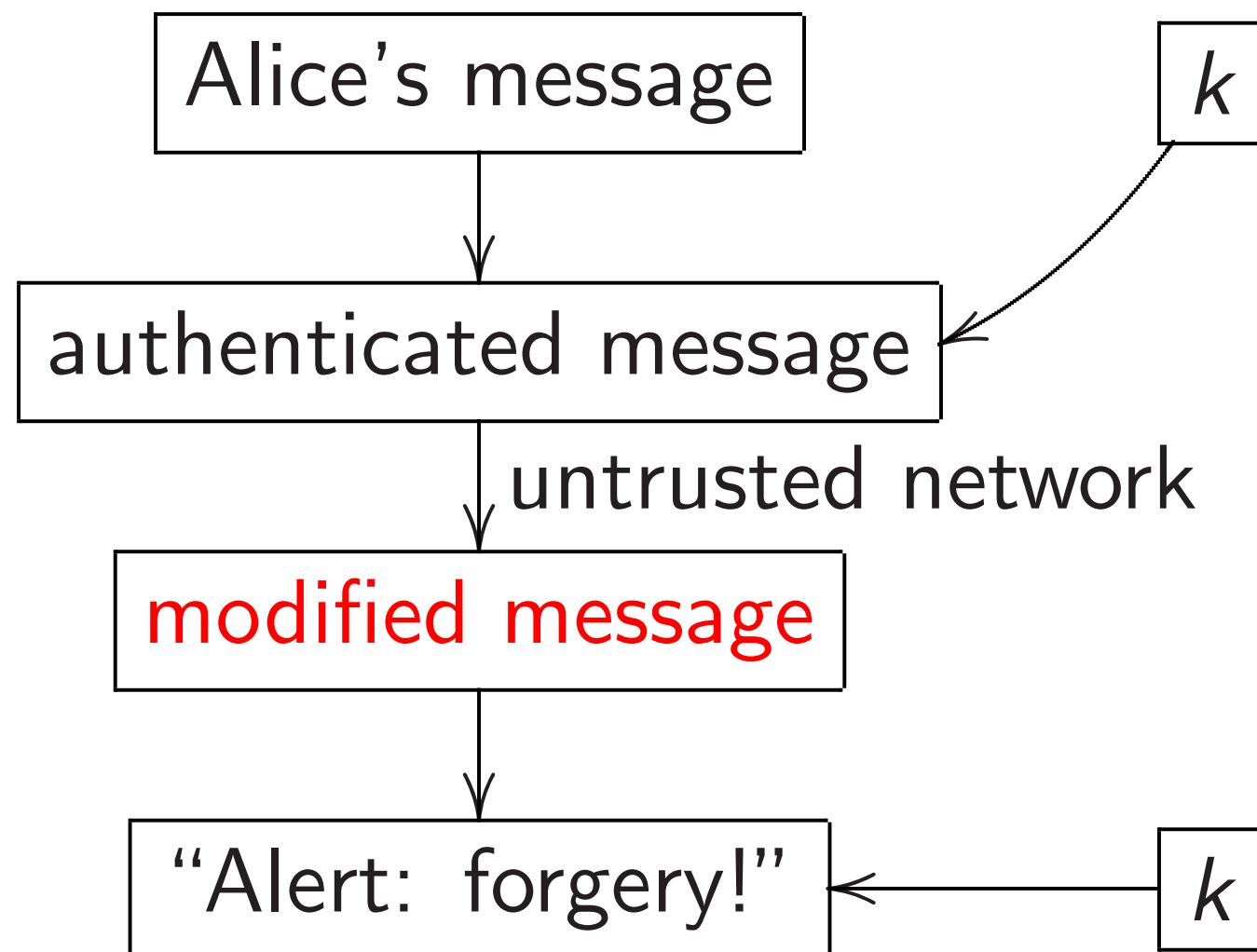
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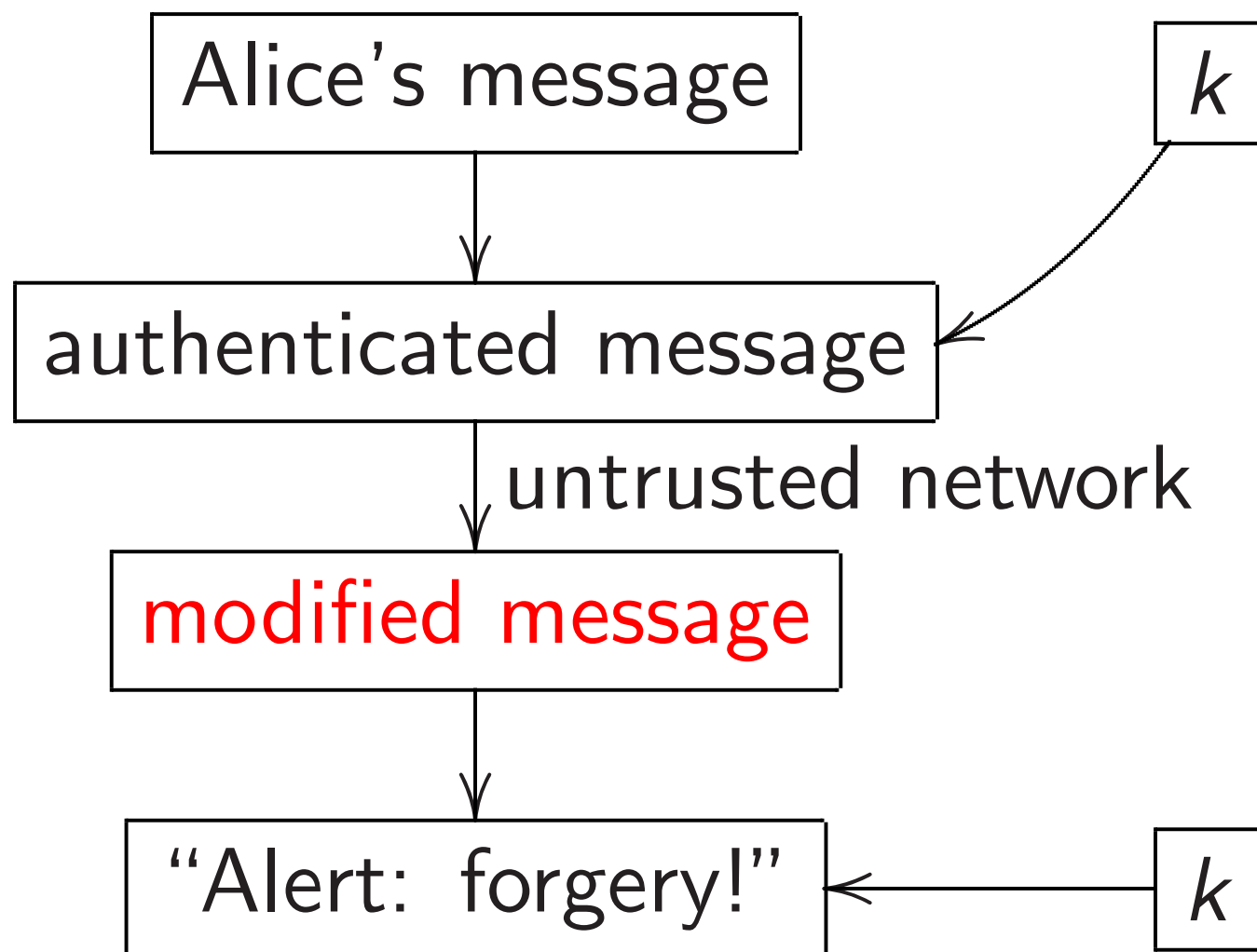
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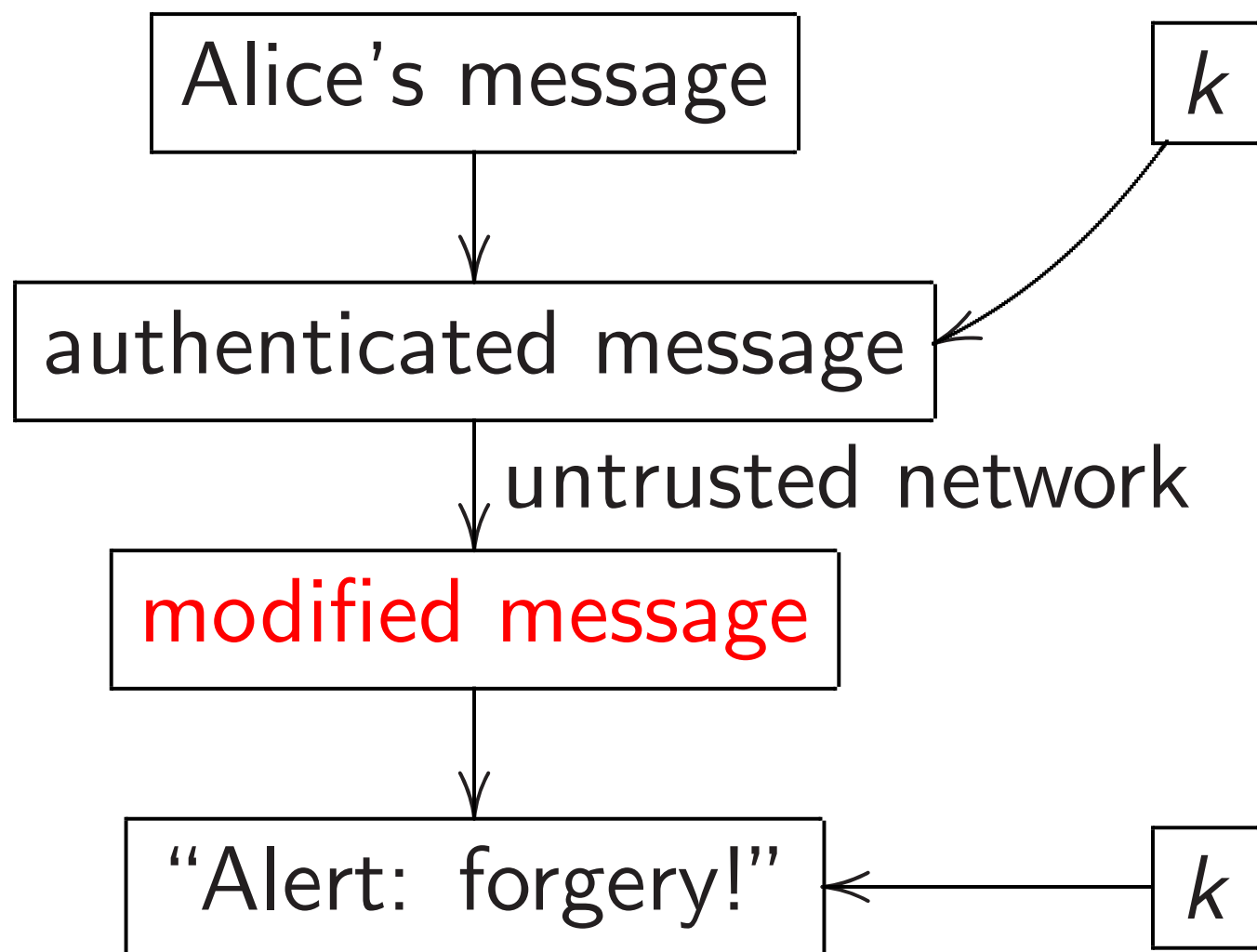
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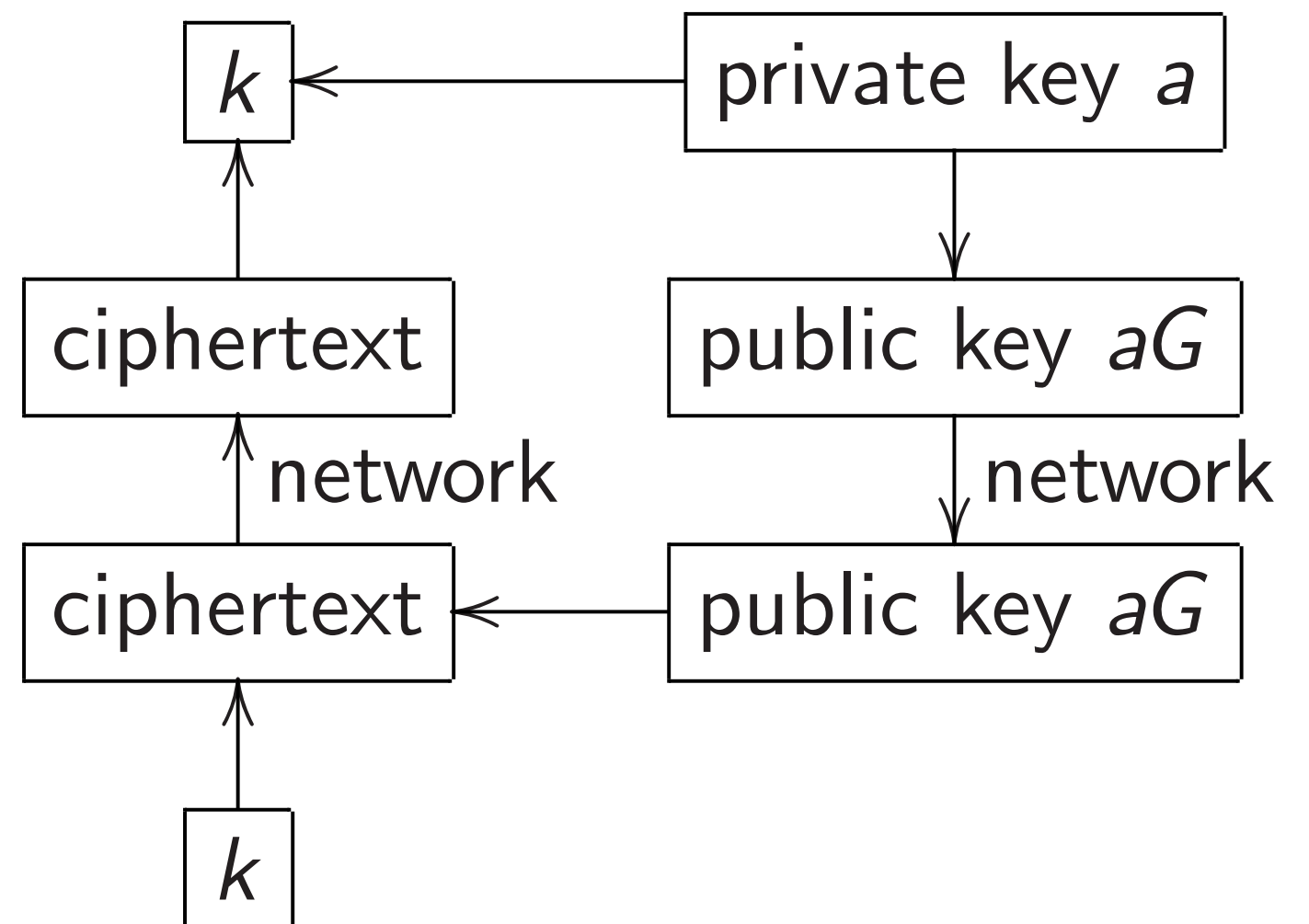


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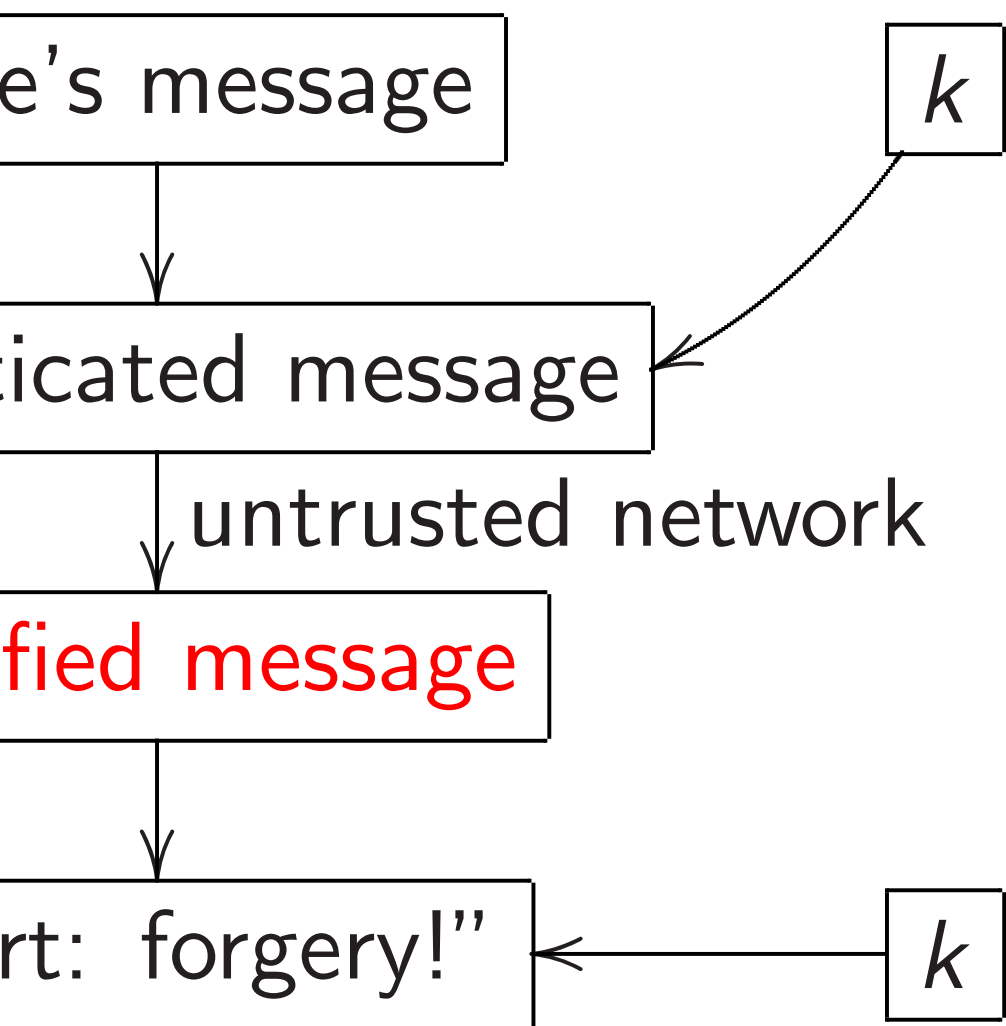
Public-key encryption.



7 of this talk: Cryptography

Does Bob's laptop know
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Graphic solution:
Message authentication codes.

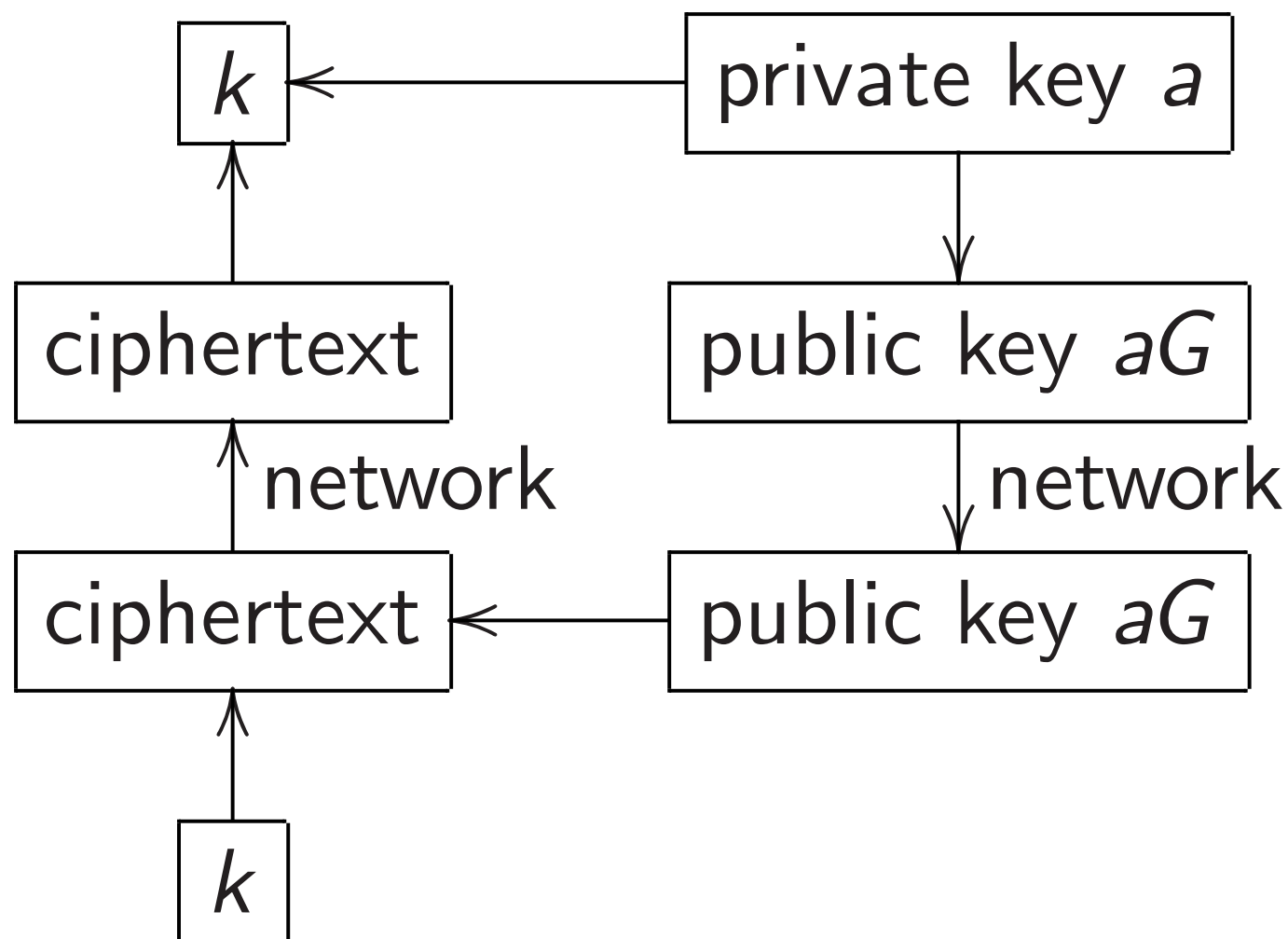


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Solution
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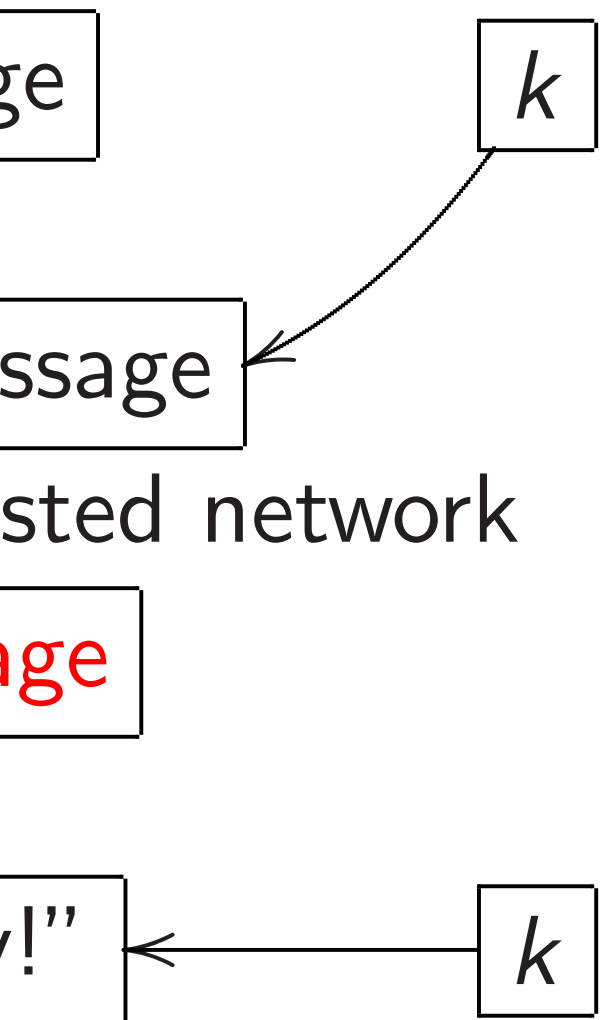
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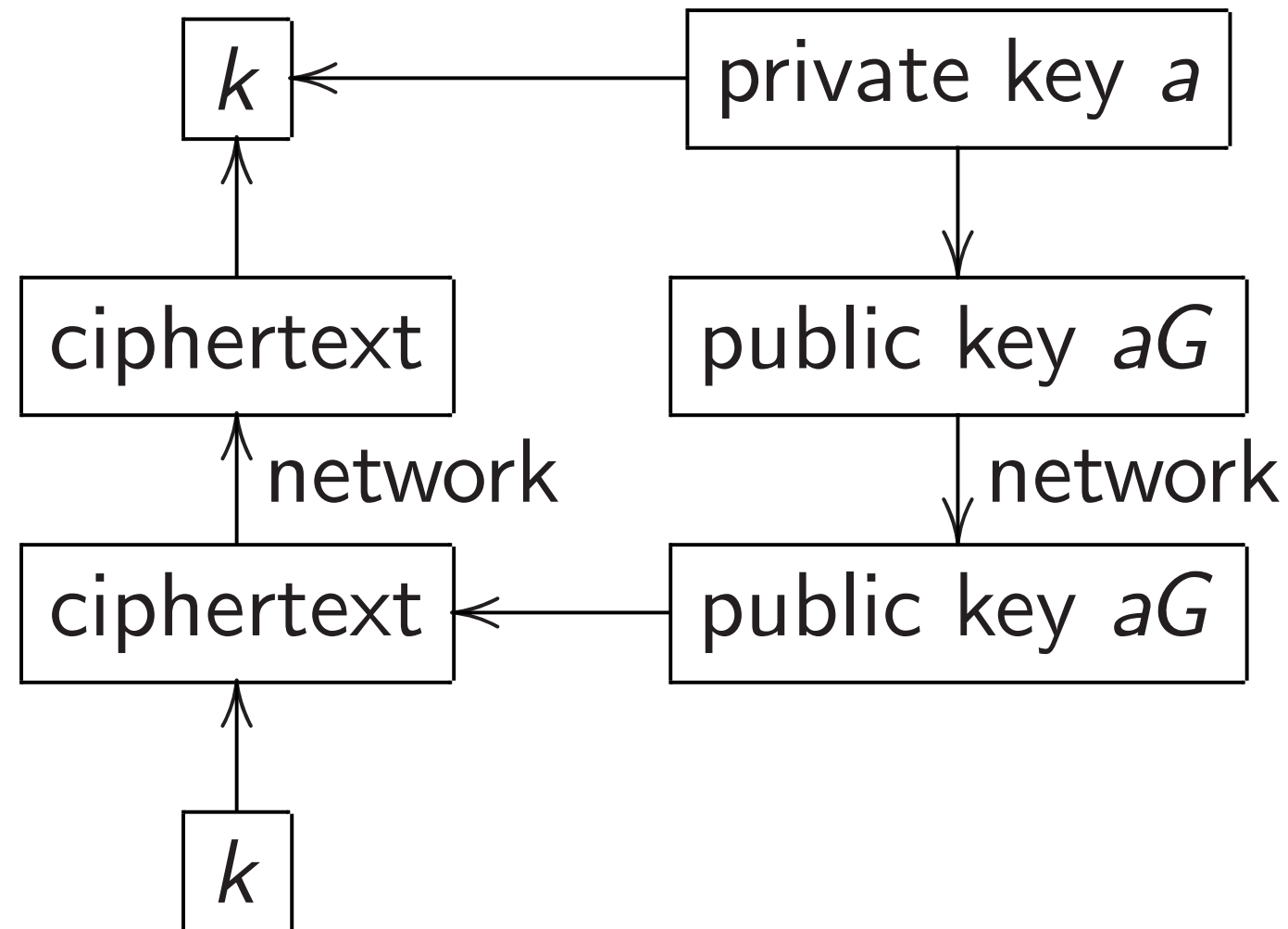


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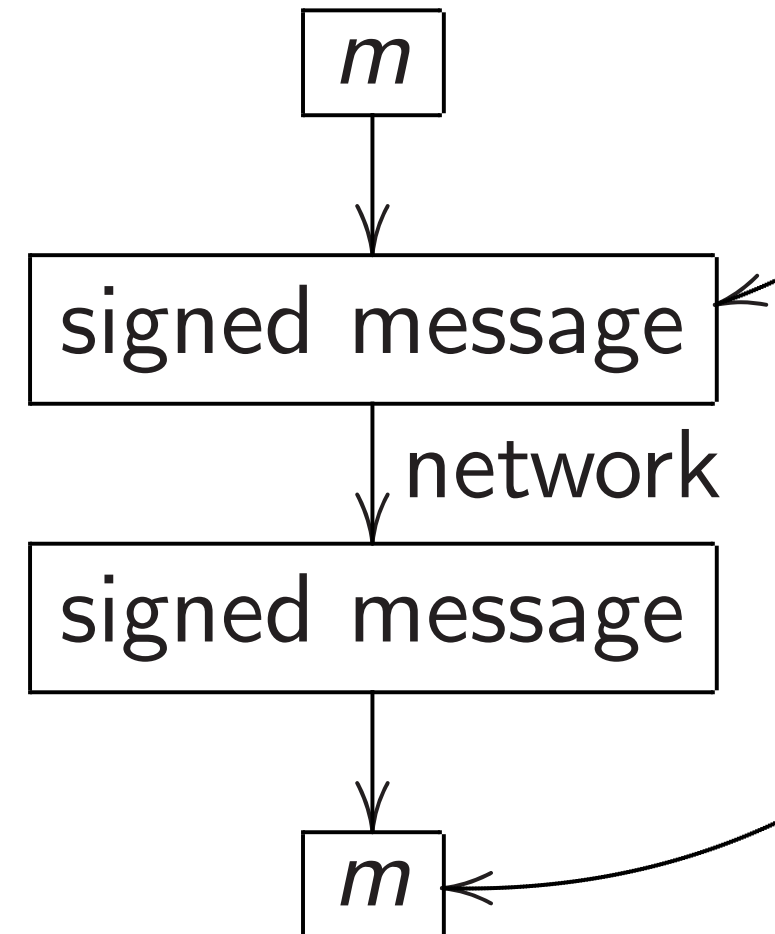
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Solution 2:
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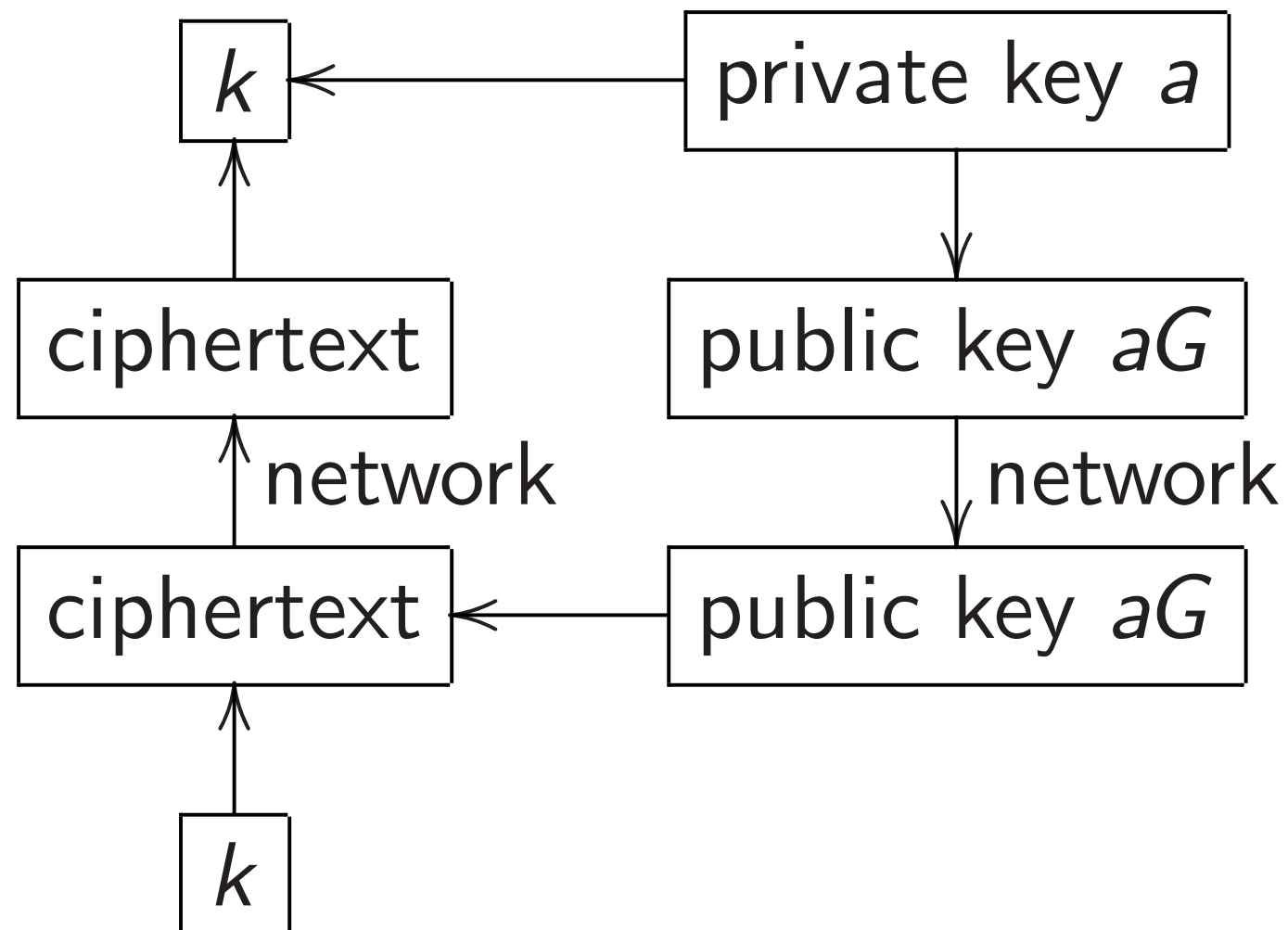
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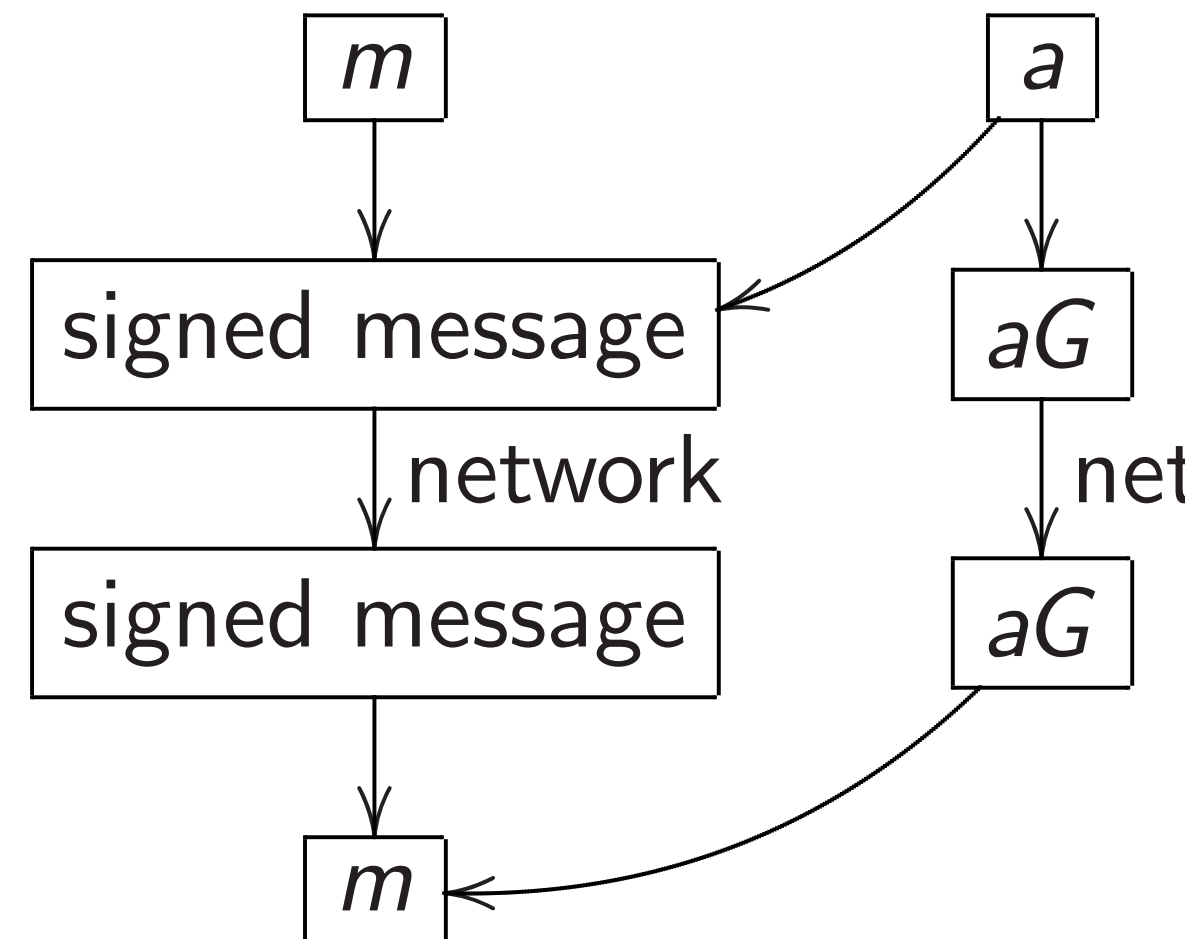
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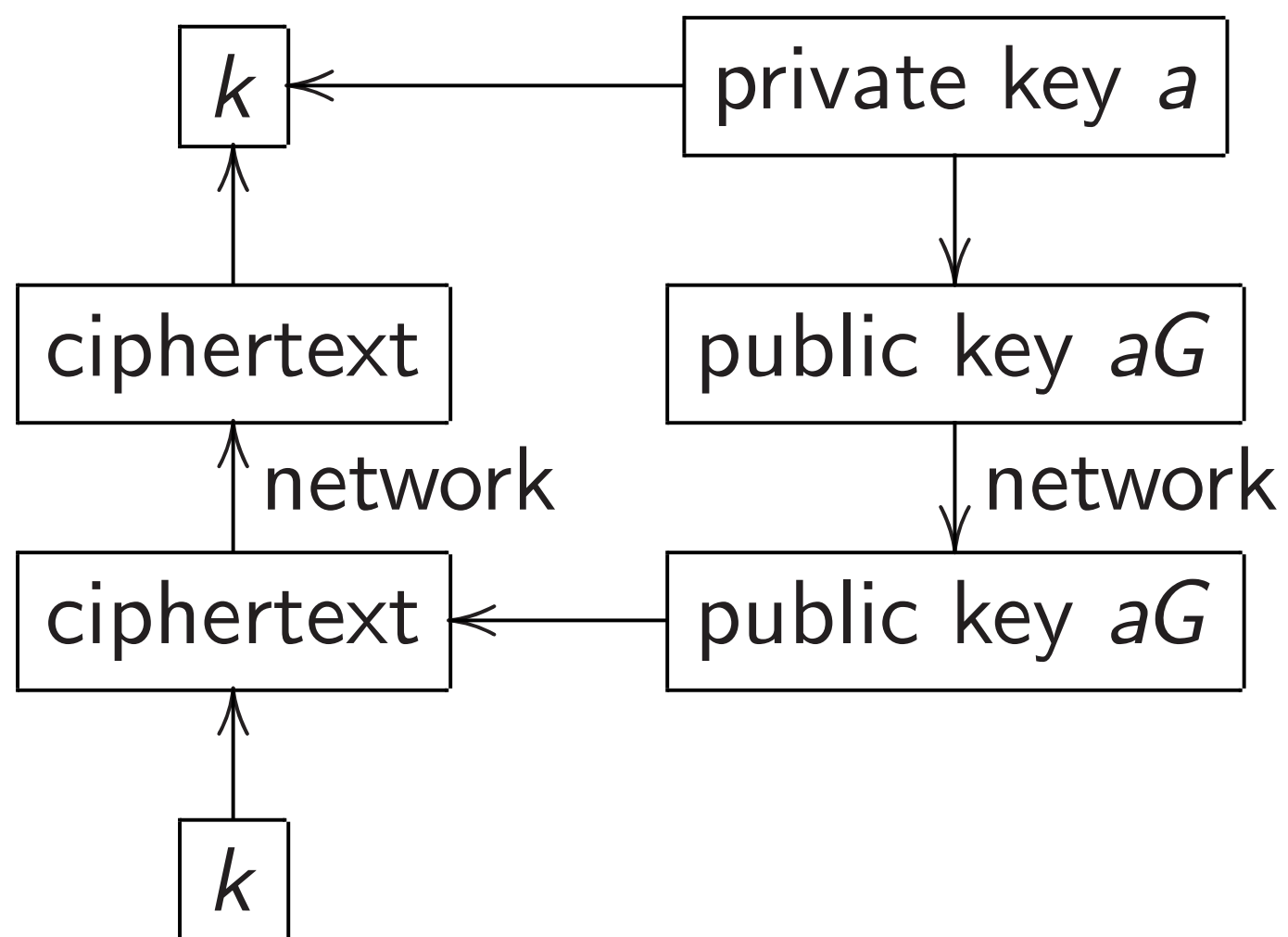


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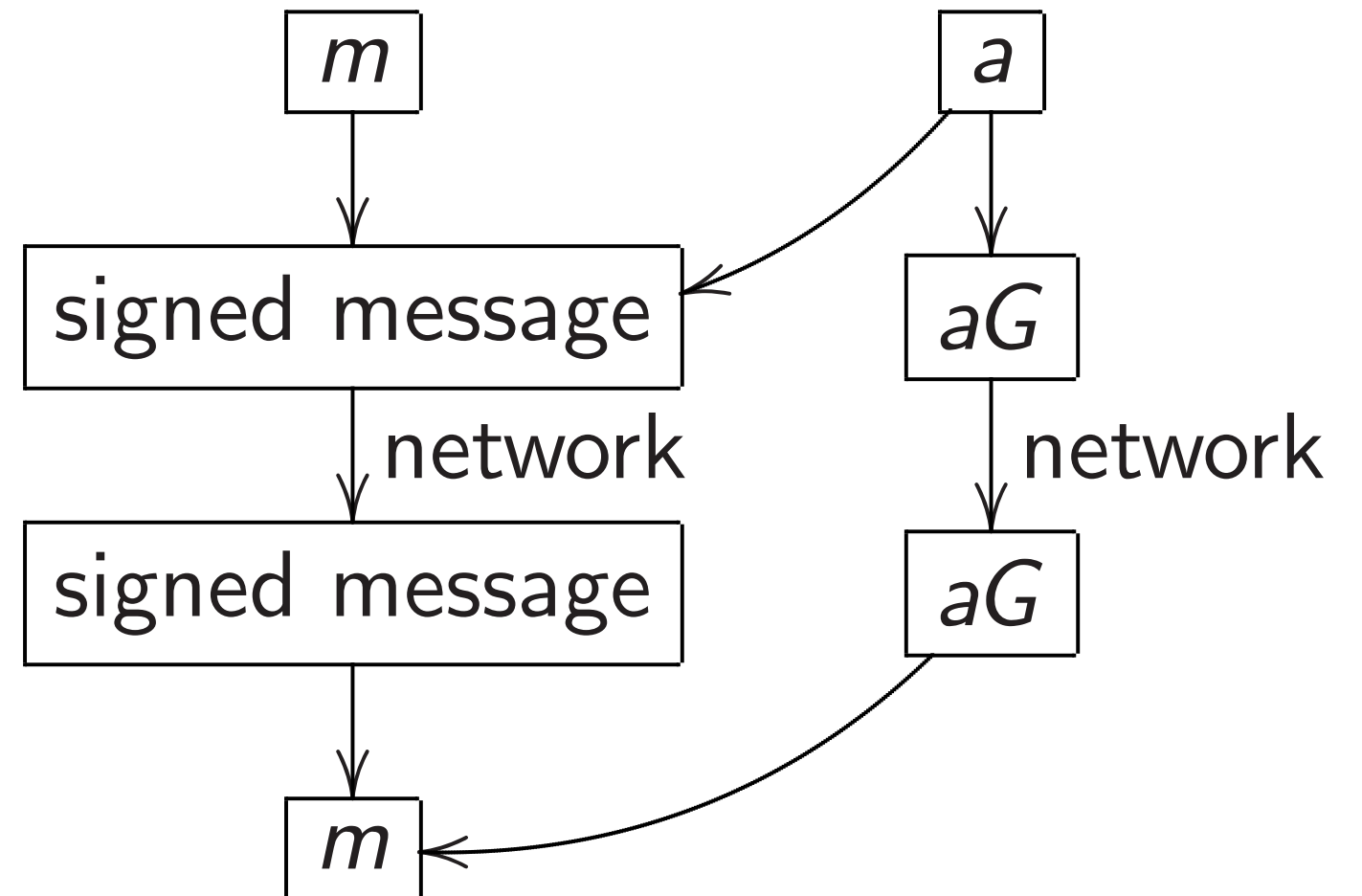
Solution 1:

Public-key encryption.



Solution 2:

Public-key signatures.

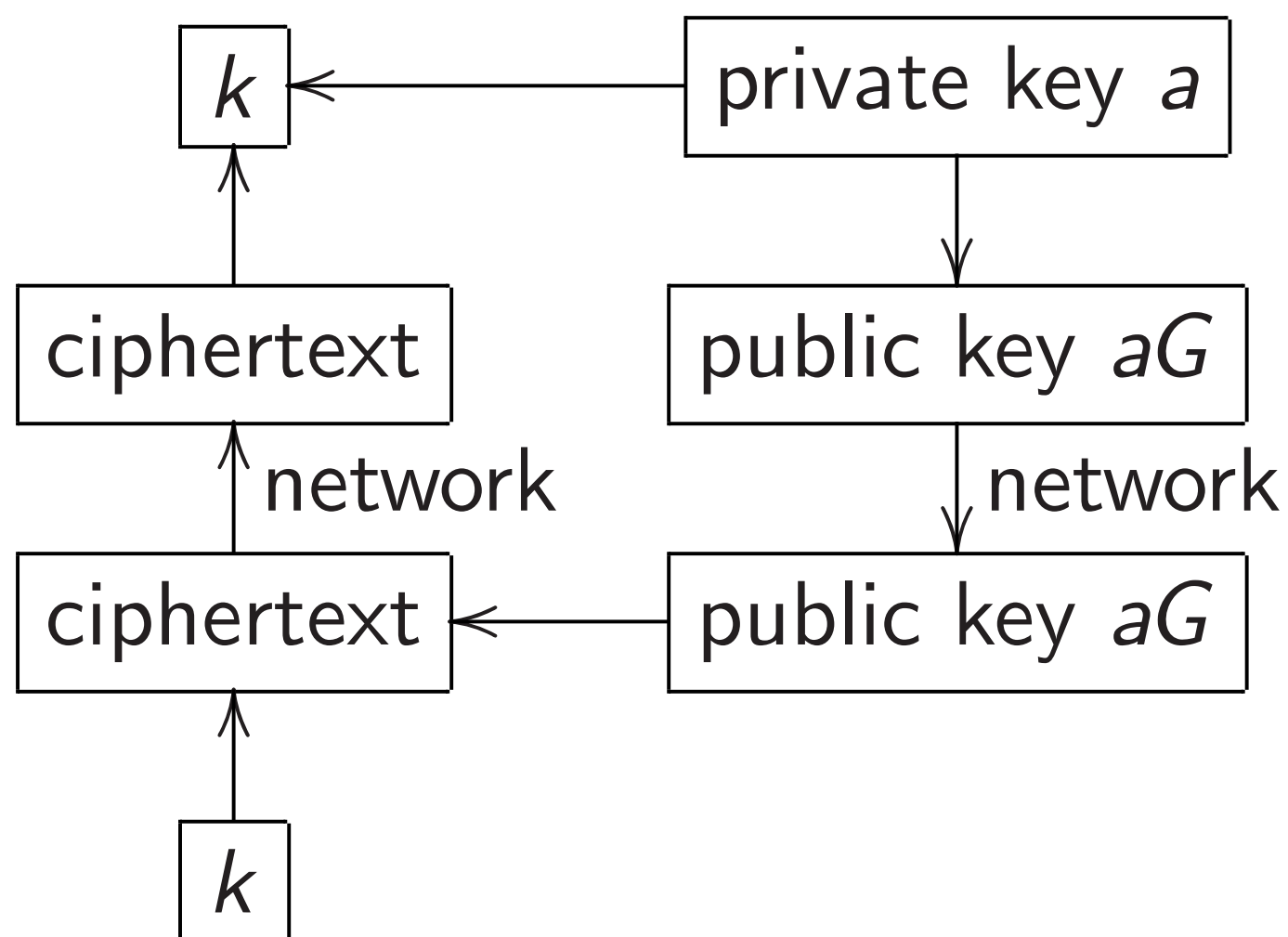


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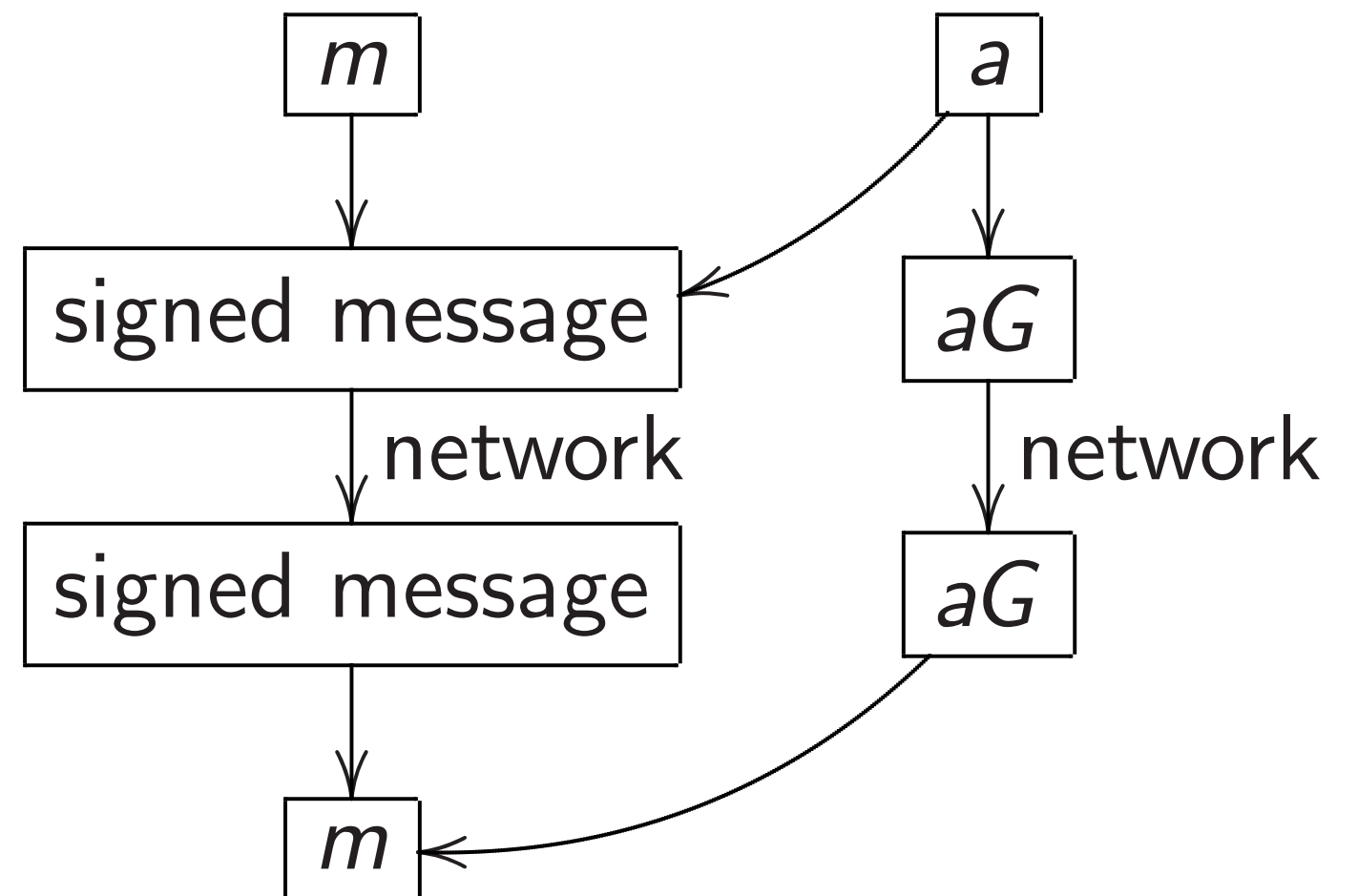
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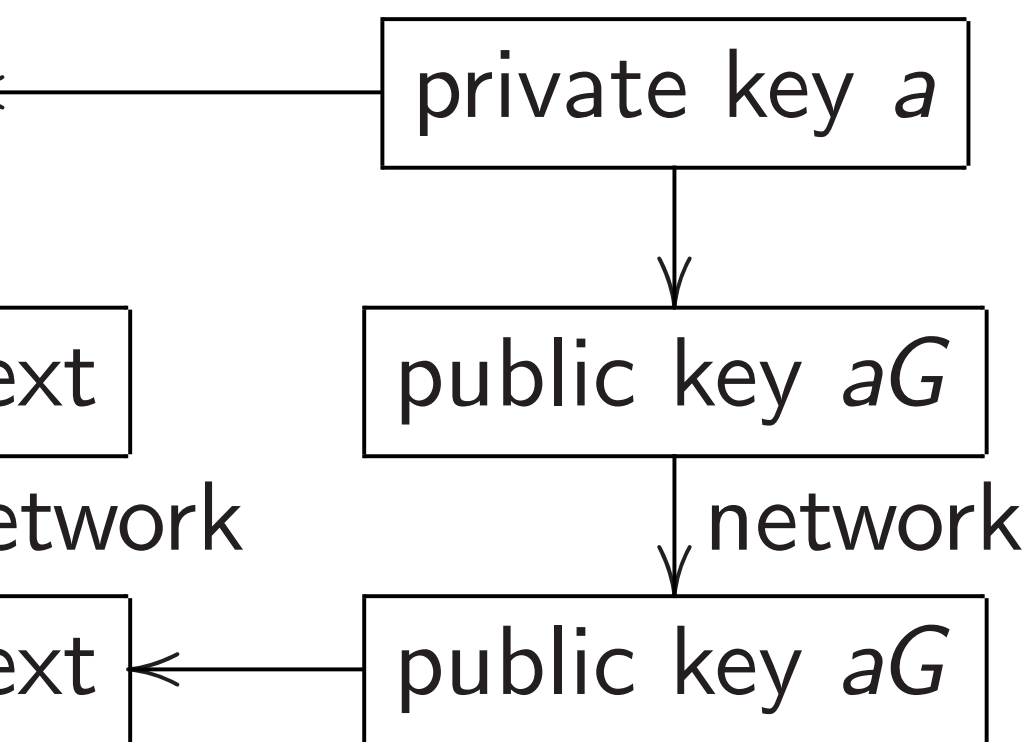


Fantasy world: software for authentication/encryption/signs is small and carefully audited \Rightarrow no cryptographic security failures.

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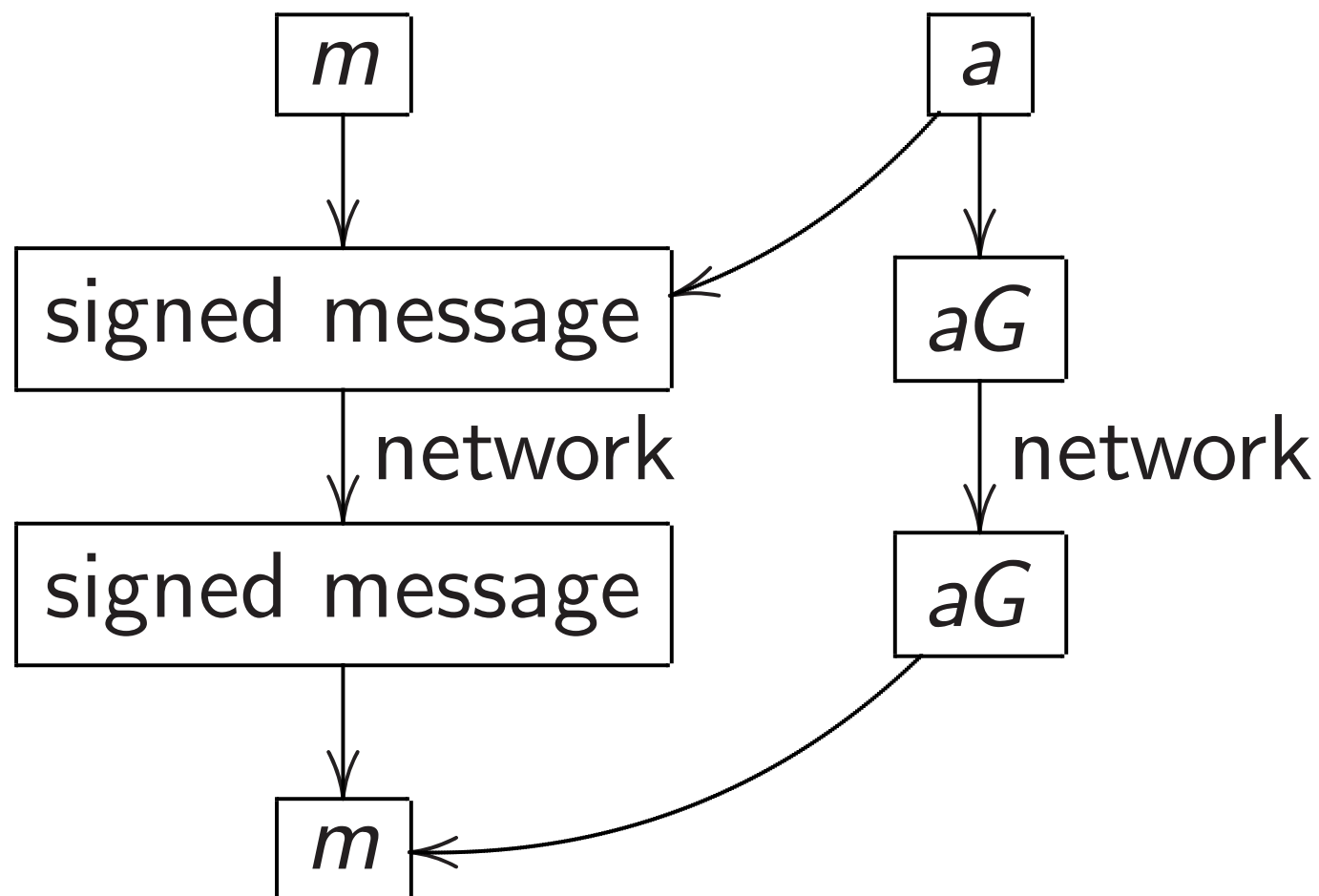
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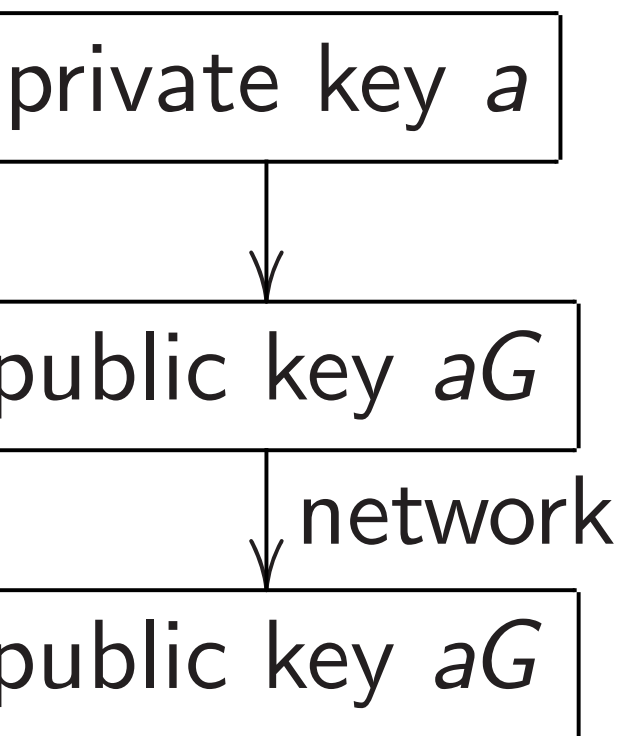
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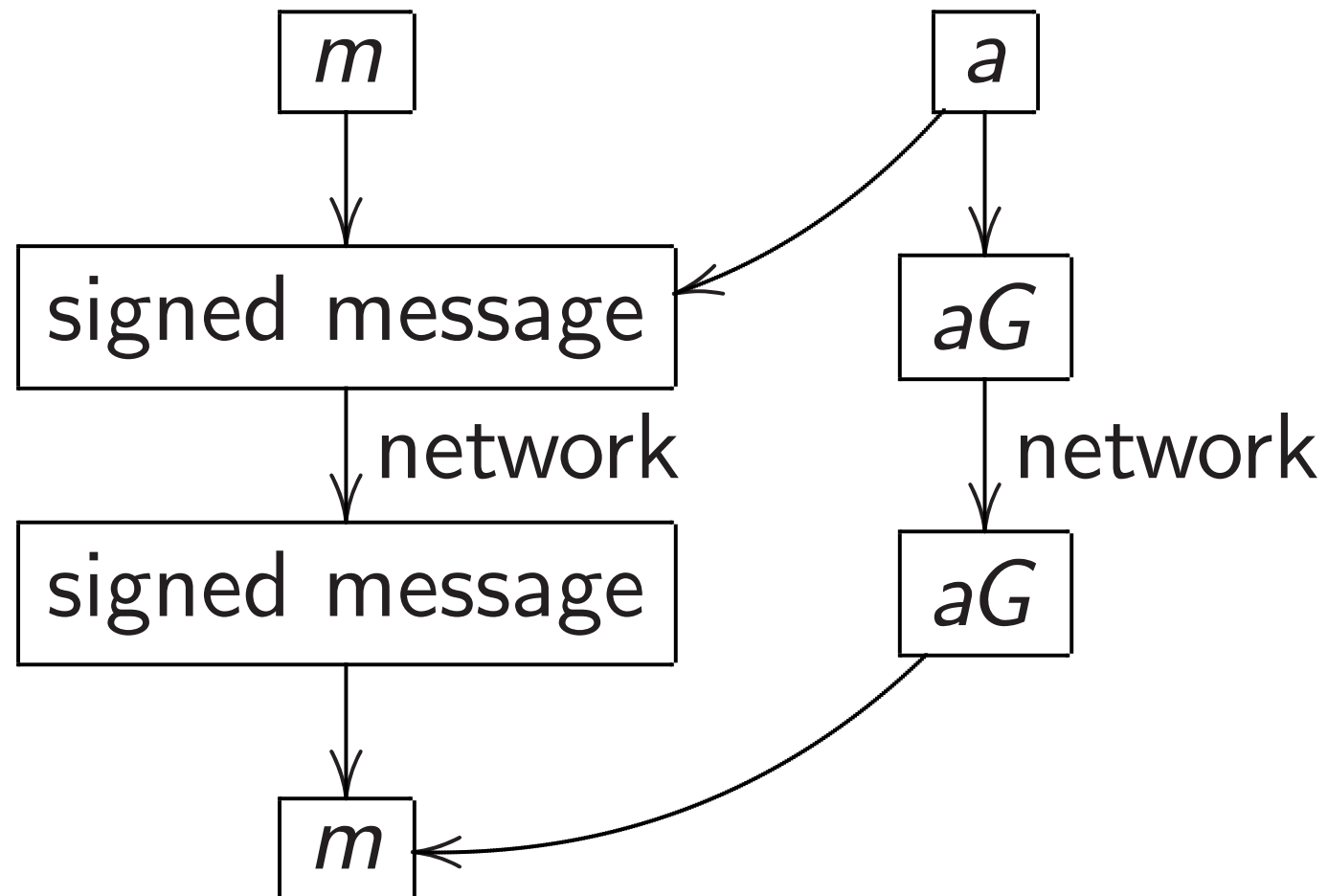
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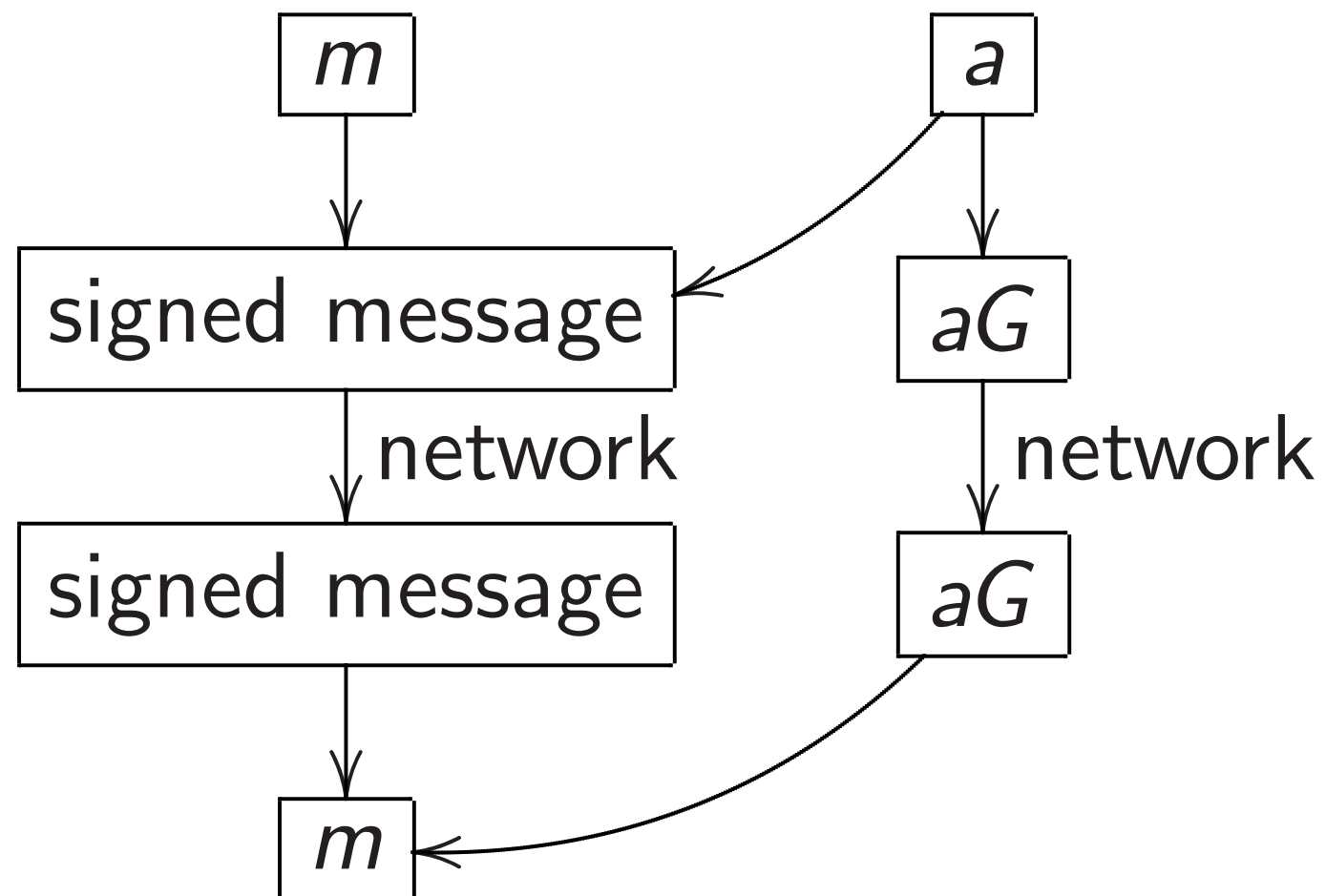
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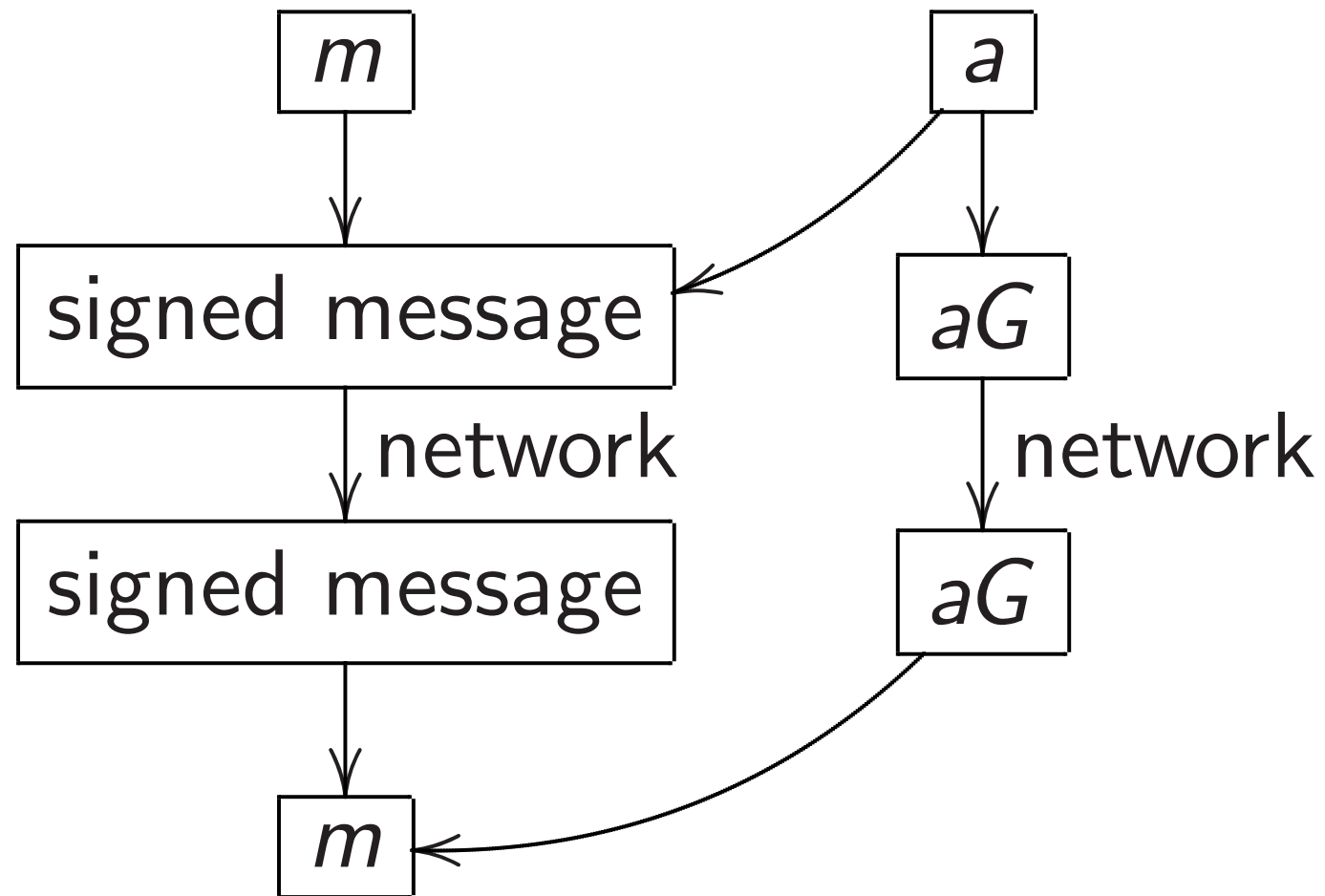
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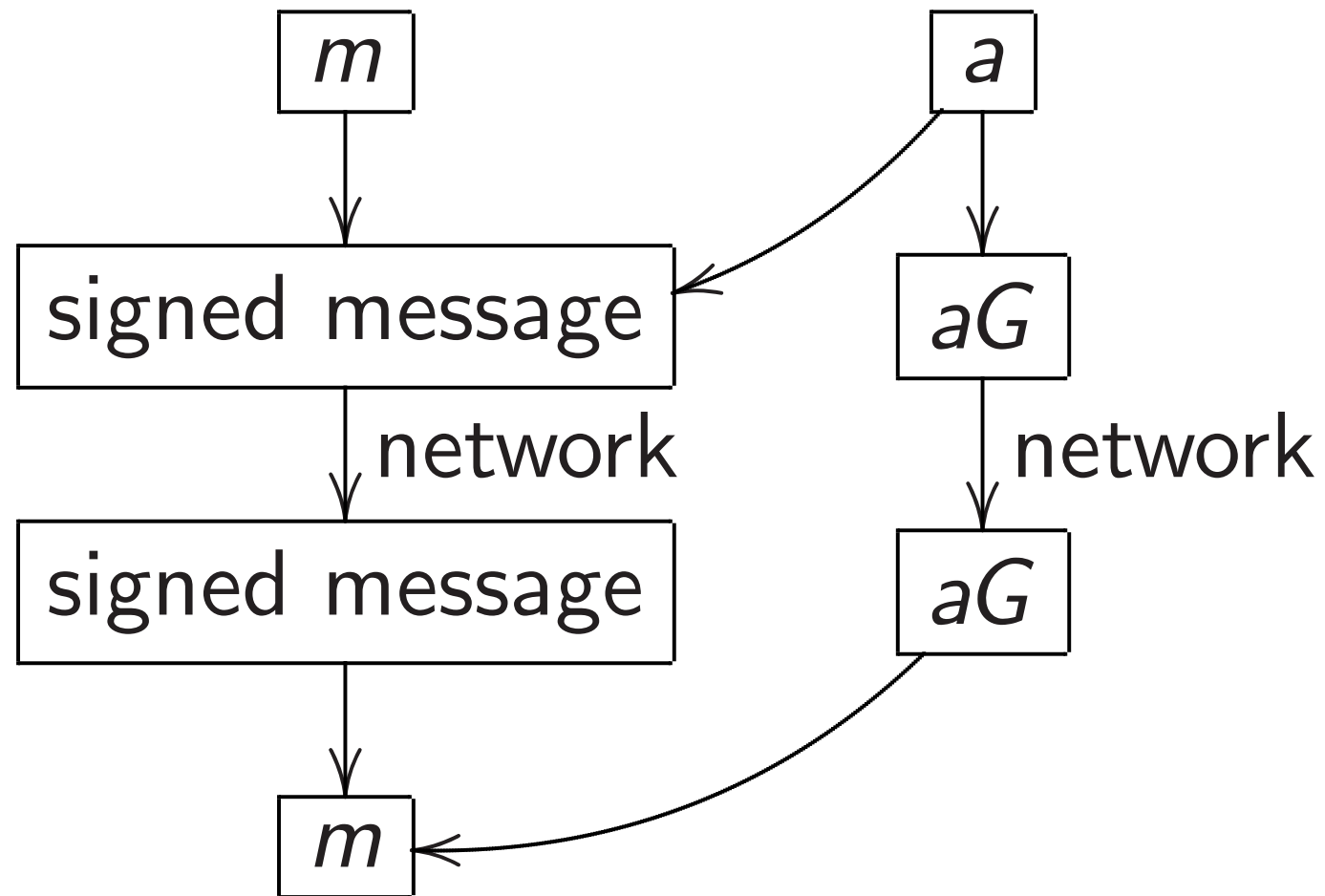
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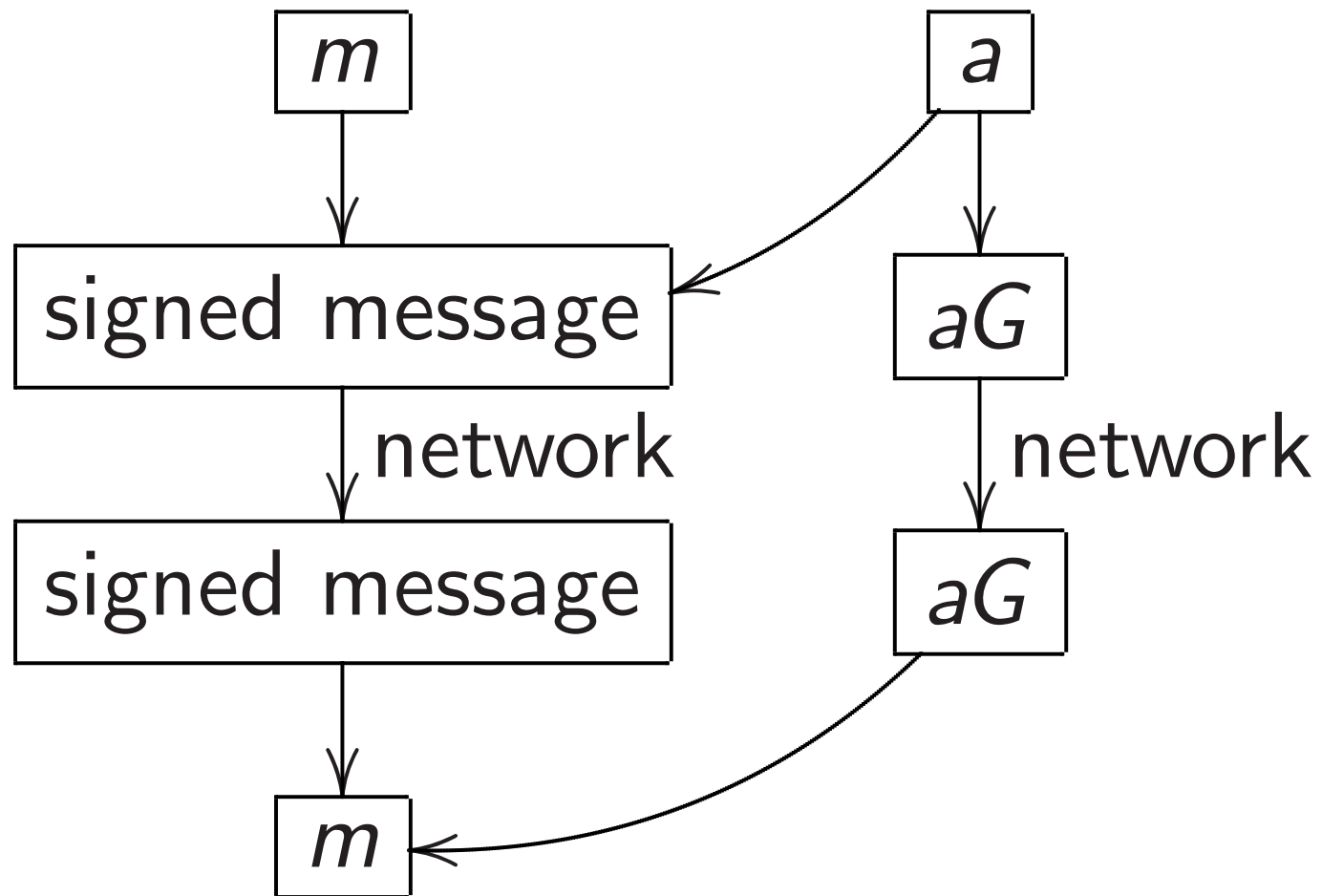
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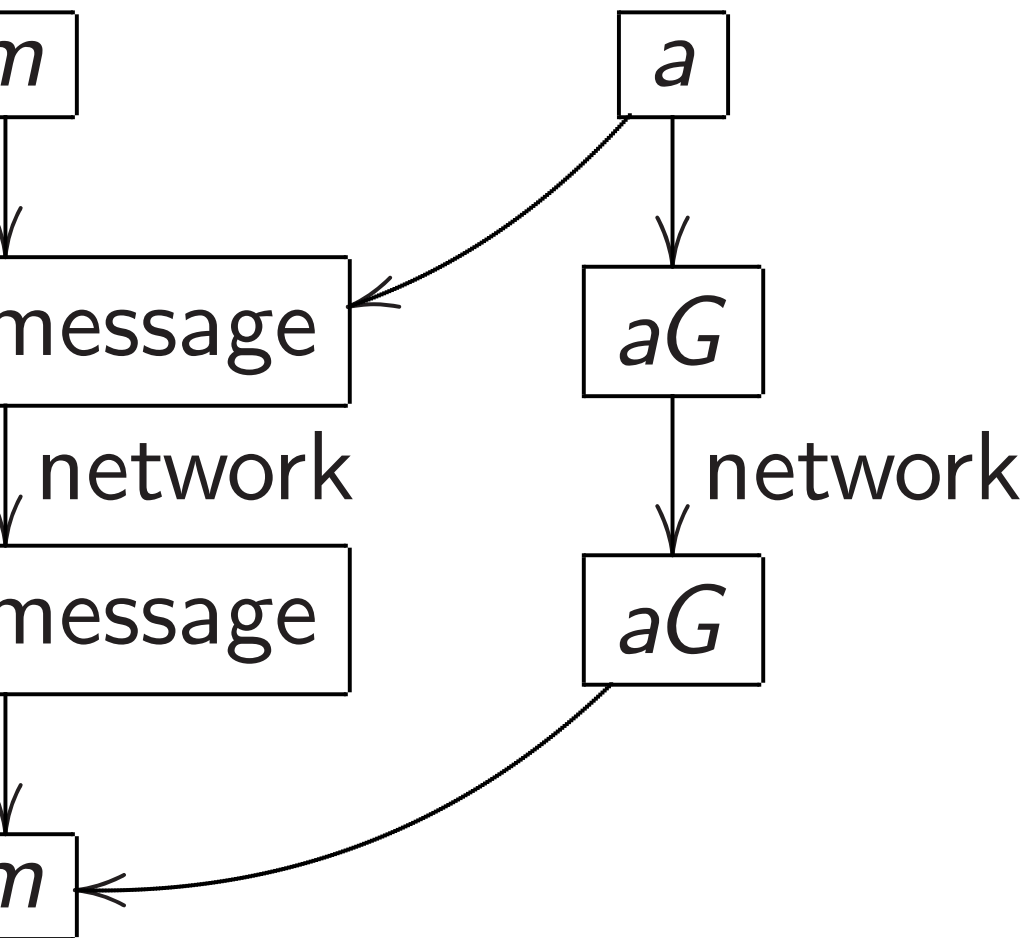
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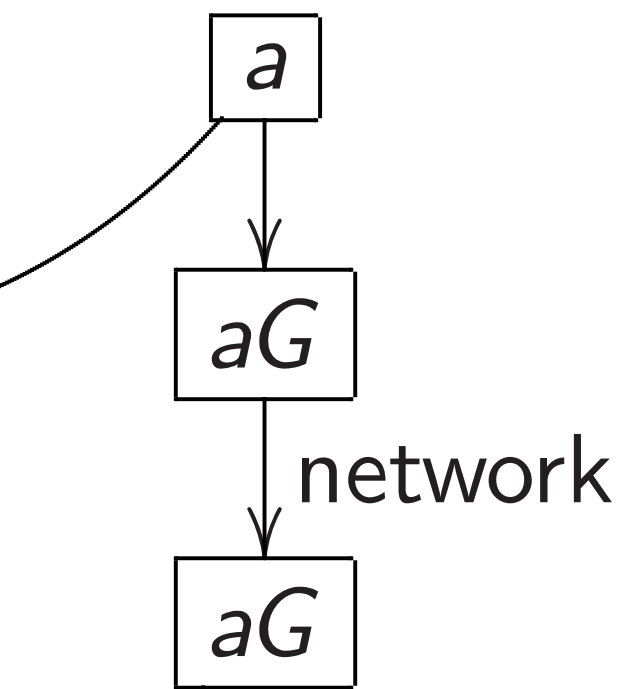
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Many years of security failures.

No confidence in future security.

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portion of CPU hardware:
variations depending on
addresses of memory locations.

branch data caching,
branch prediction, branch
target caching,
branch target cache banks,
branch target load forwarding,
branch target prediction, etc.

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Case study: Const

Subroutine in (e.g.

Classic McEliece,

Gravity-SPHINCS,

LEDAPkc, NTRU

sort array of secrets

e.g. sort 768 32-bit

Typical sorting algo

merge sort, quicks

choose load/store

based on secret da

also branch based

How to sort secrets

without any secrets

The “constant-time” solution:

Don't give any secrets
to this portion of the CPU.
(1987 Goldreich, 1990 Ostrovsky:
Oblivious RAM; 2004 Bernstein:
domain-specific for better speed)

TCB analysis: Need this portion
of the CPU to be correct, but
don't need it to keep secrets.
Makes auditing much easier.

Good match for attitude and
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Intel issues errata for correctness
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Case study: Constant-time s

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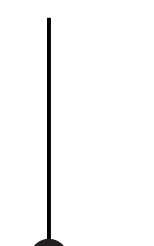
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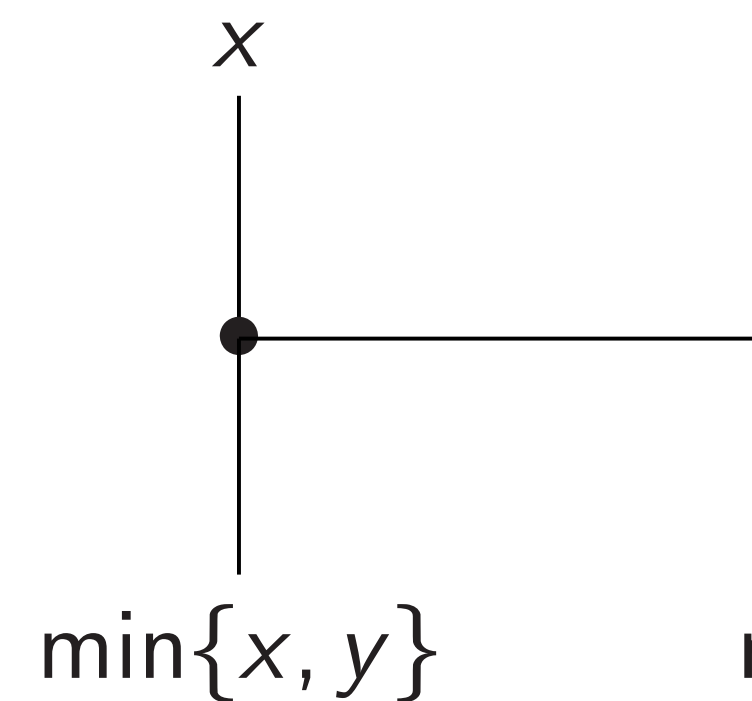
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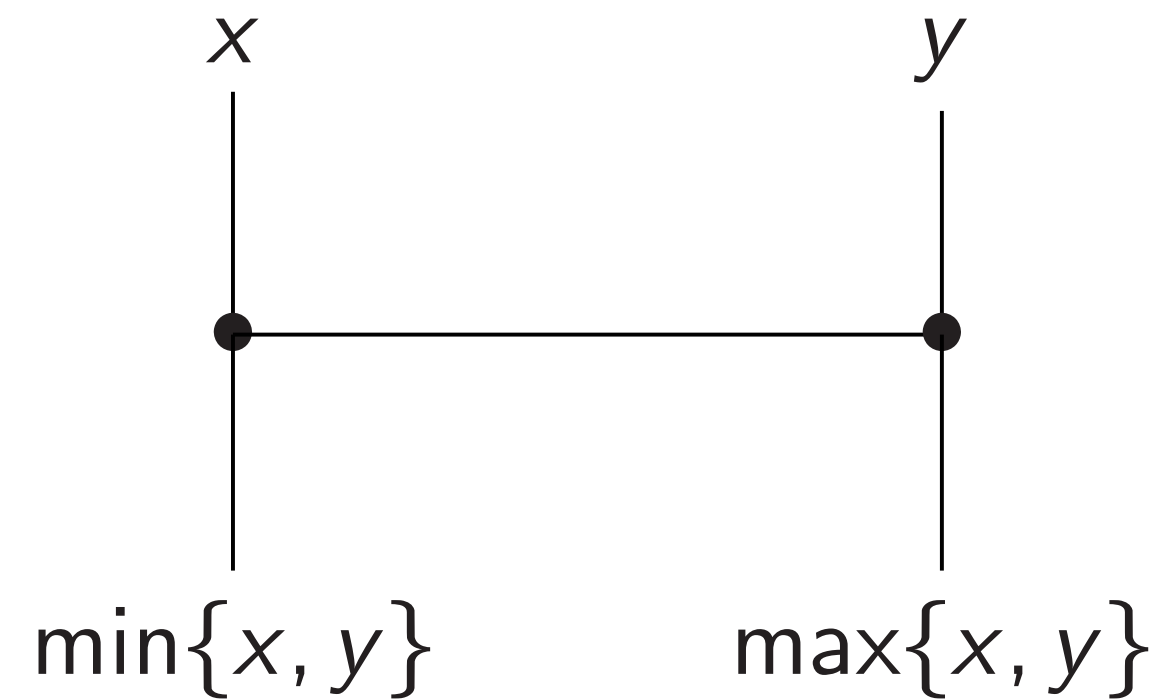
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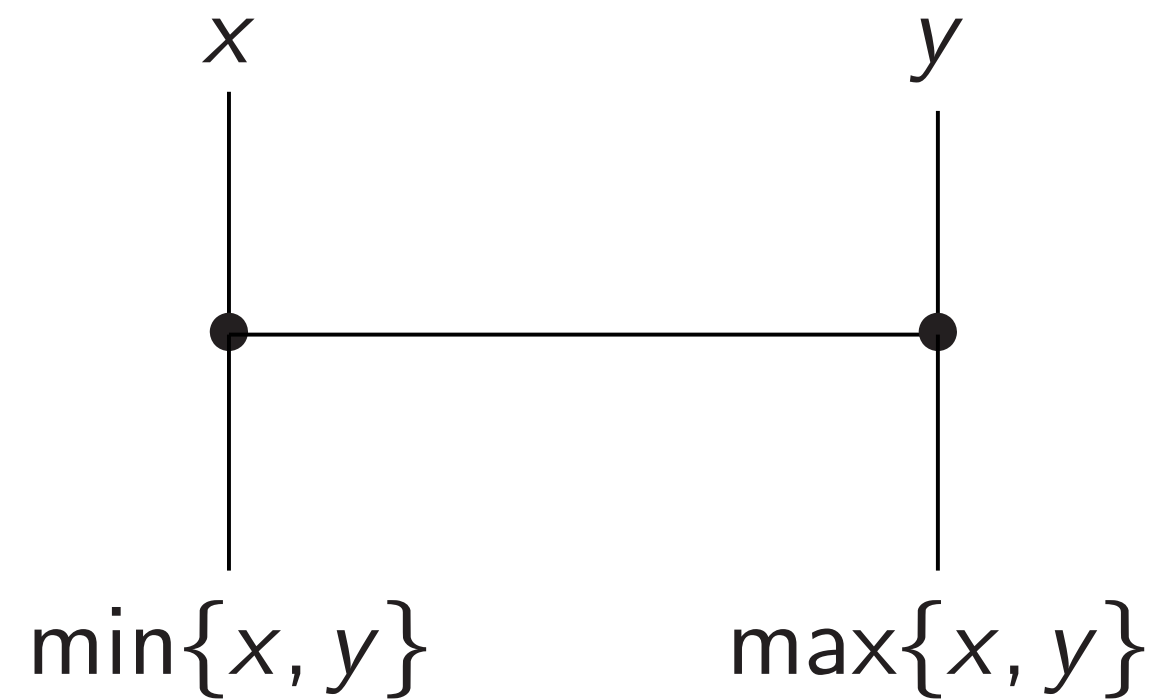
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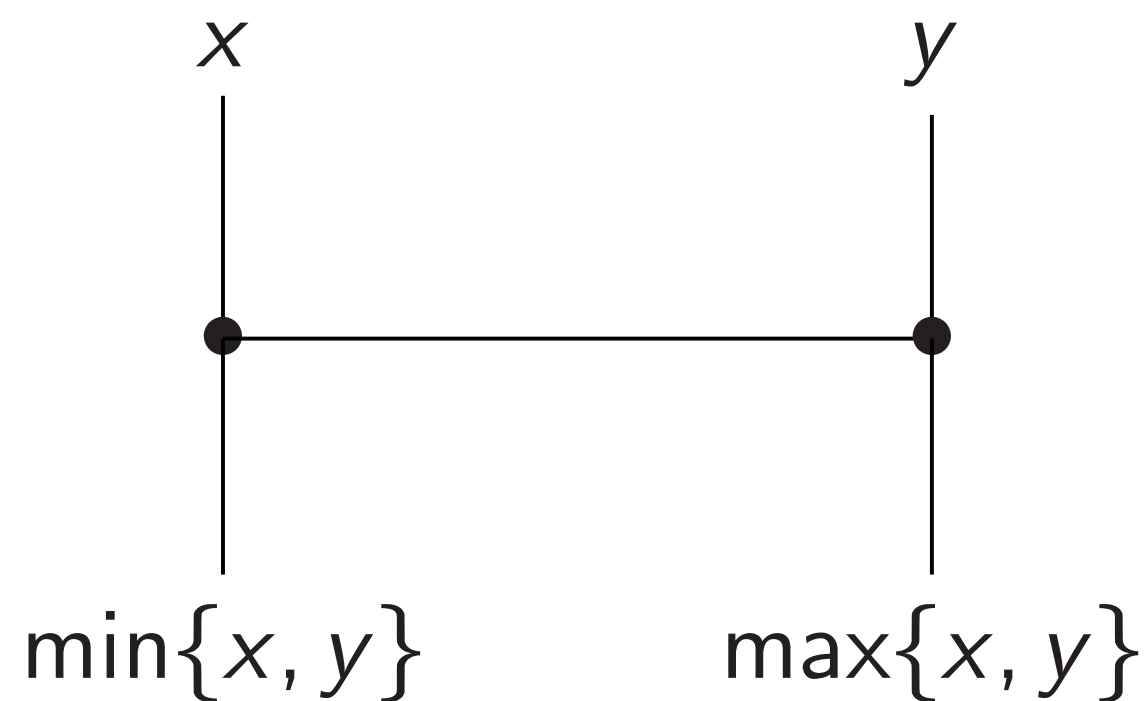
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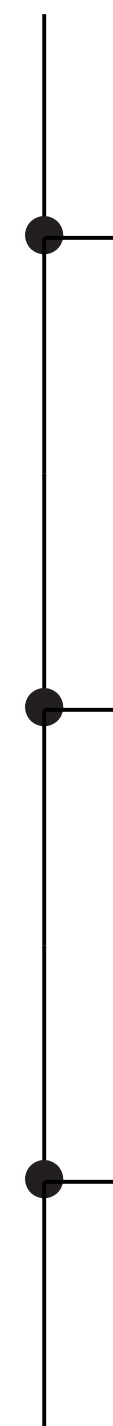
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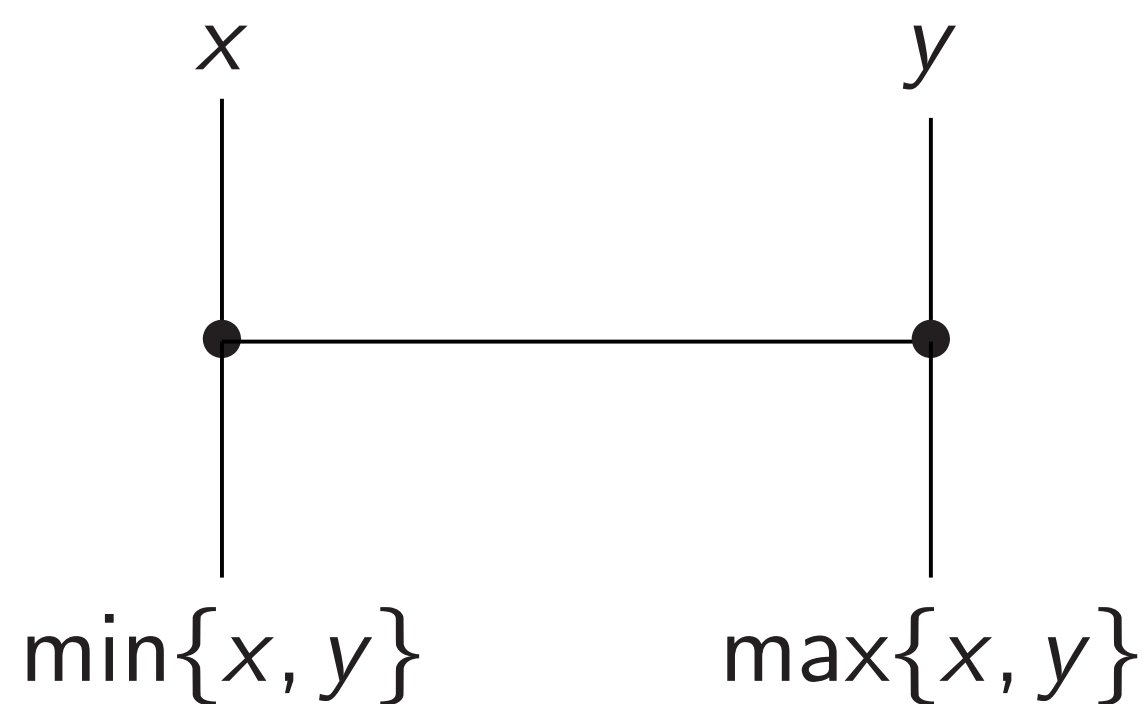
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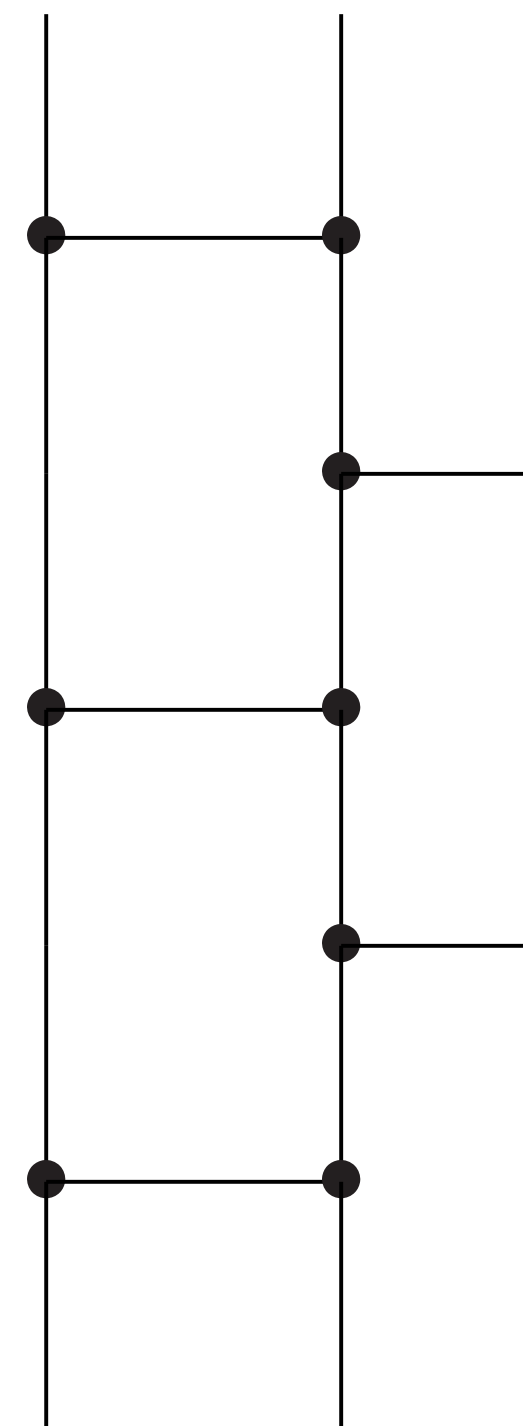
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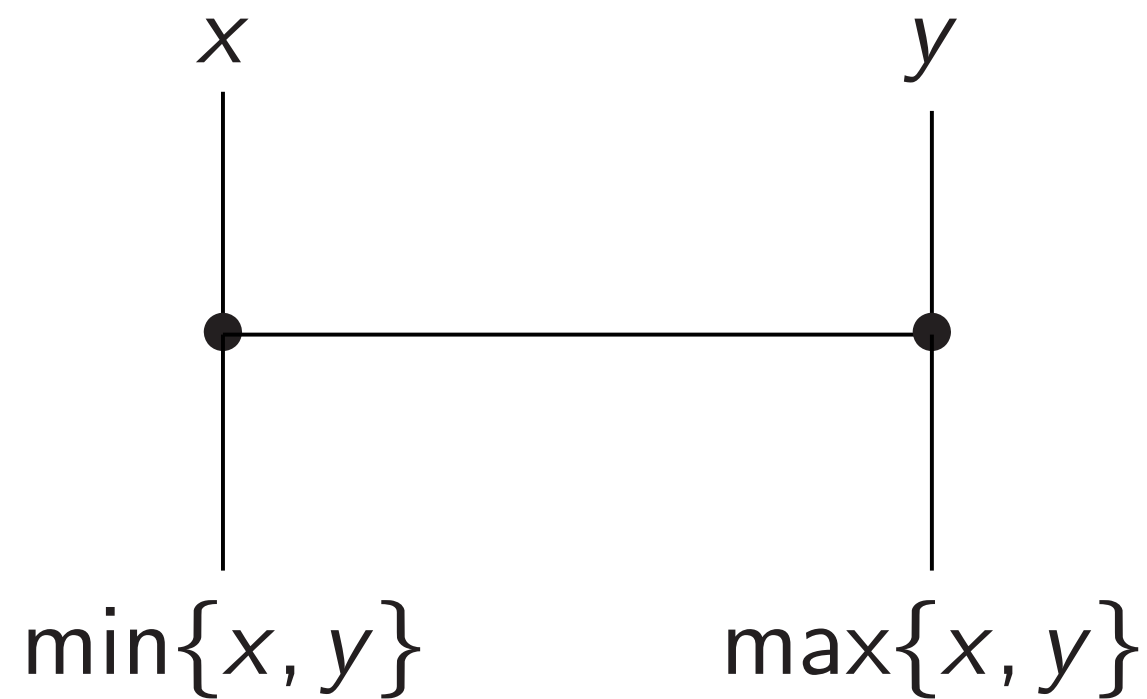
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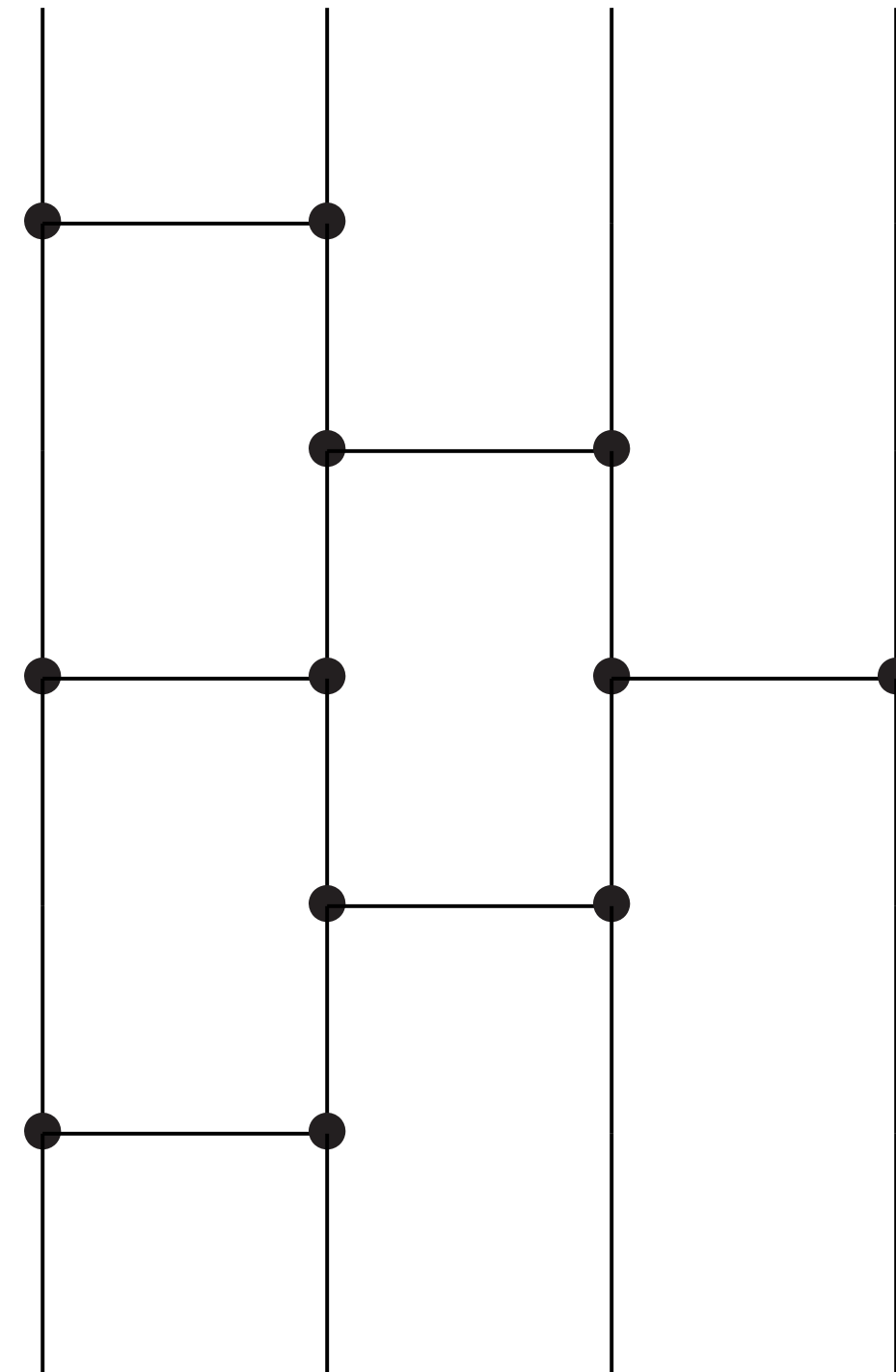
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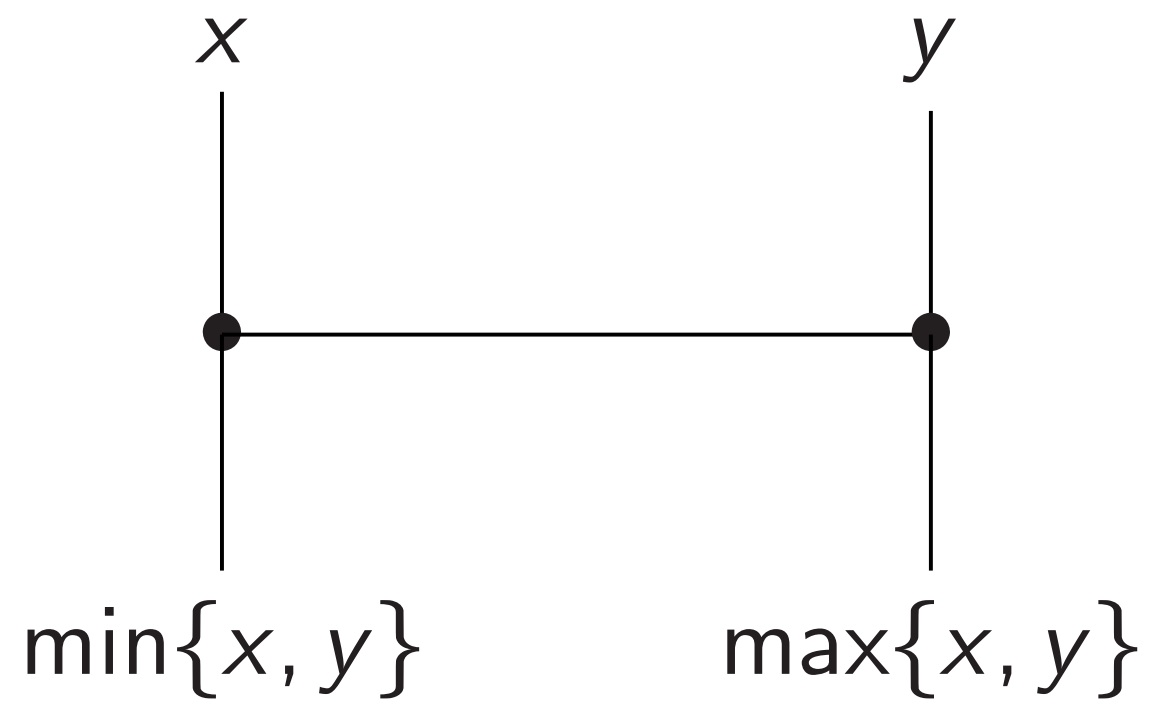
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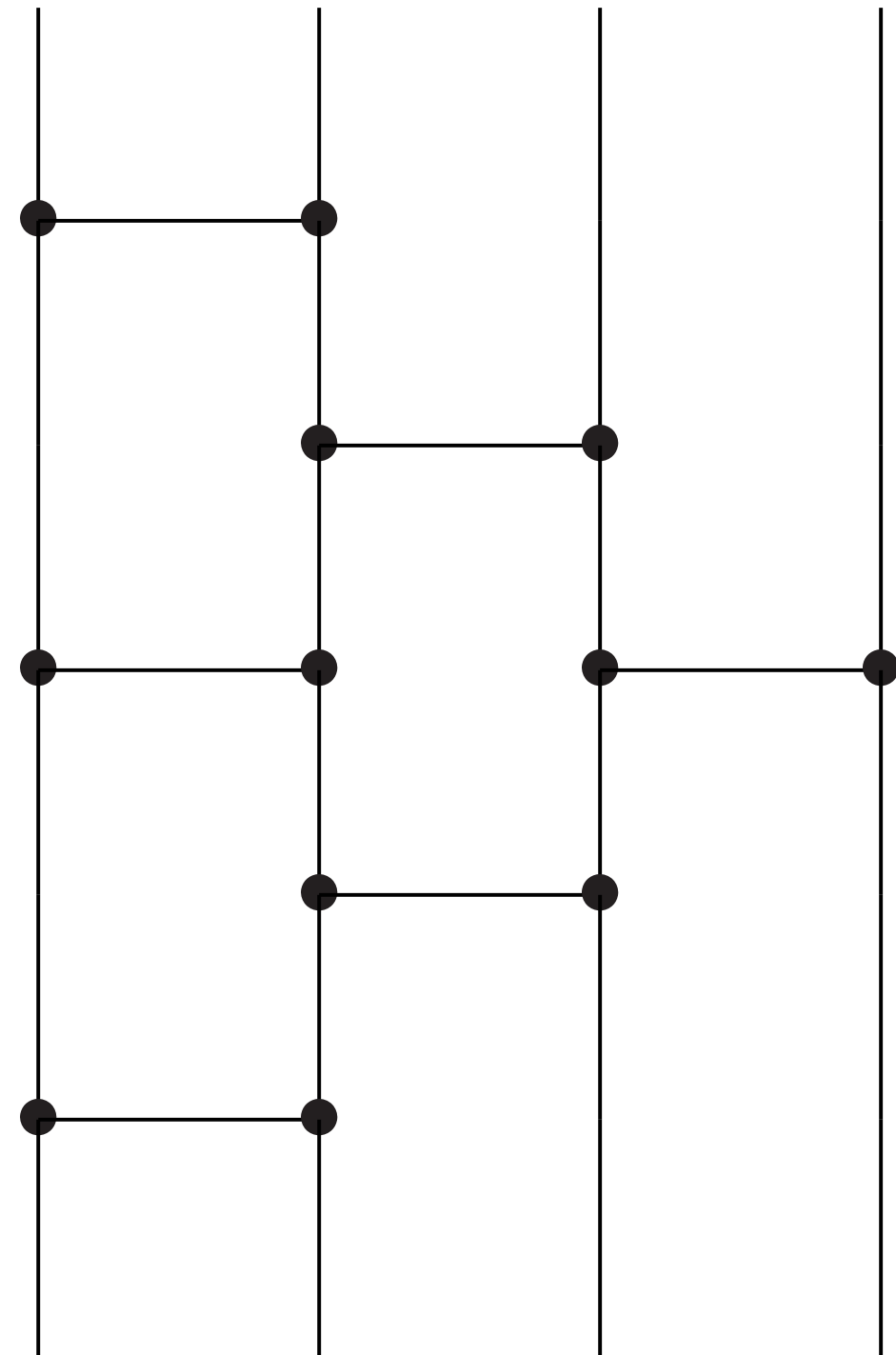
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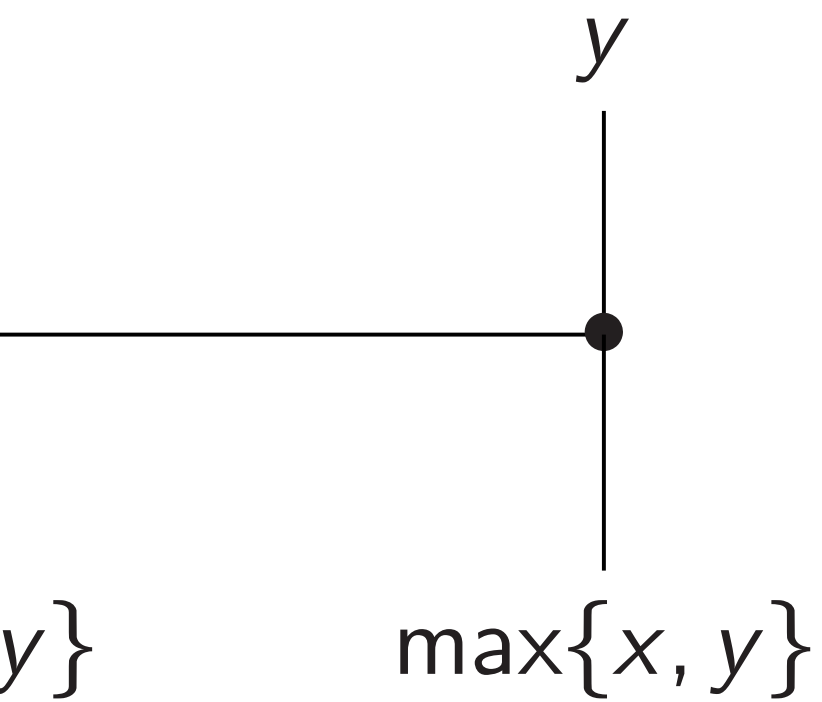
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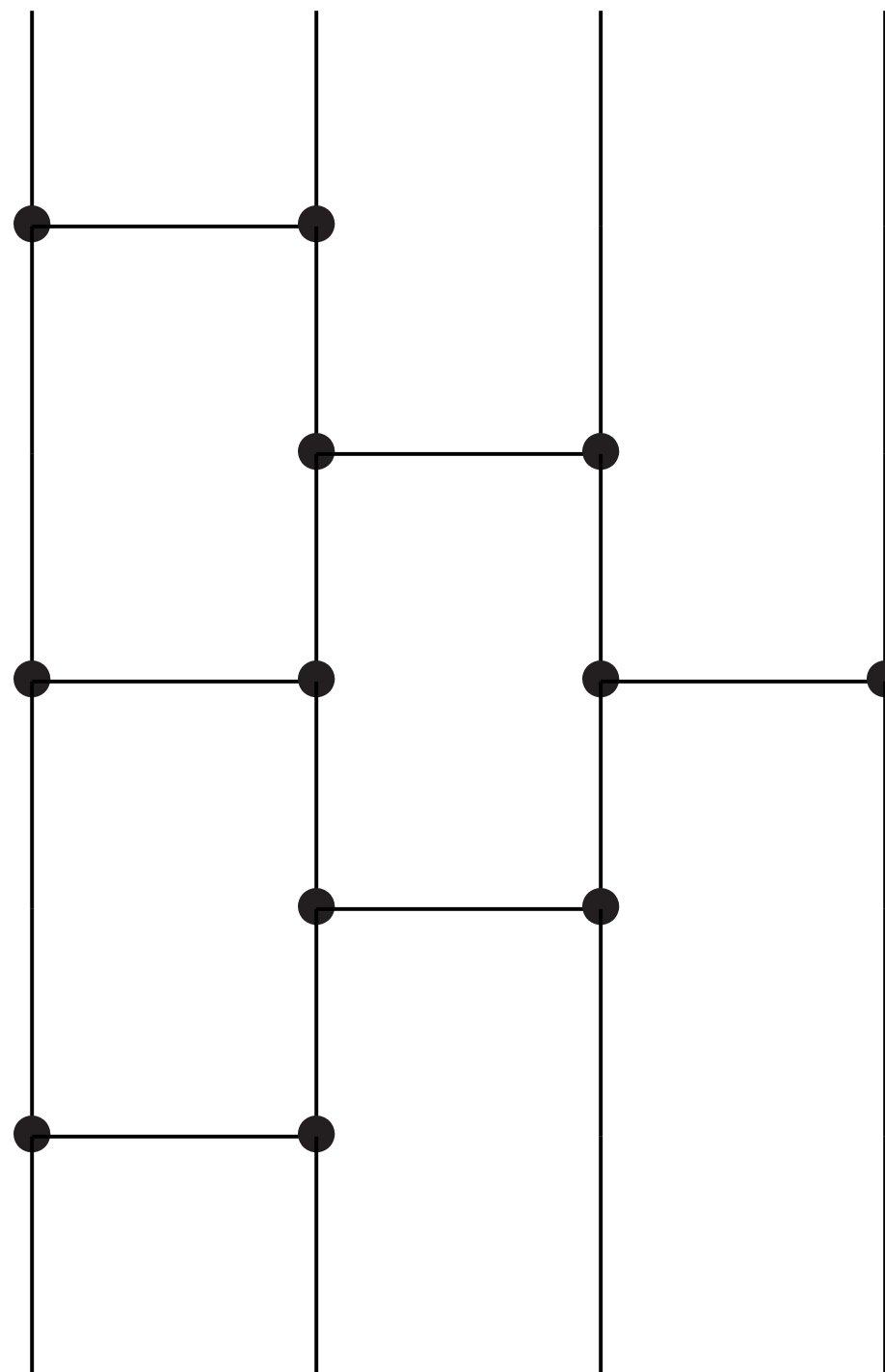
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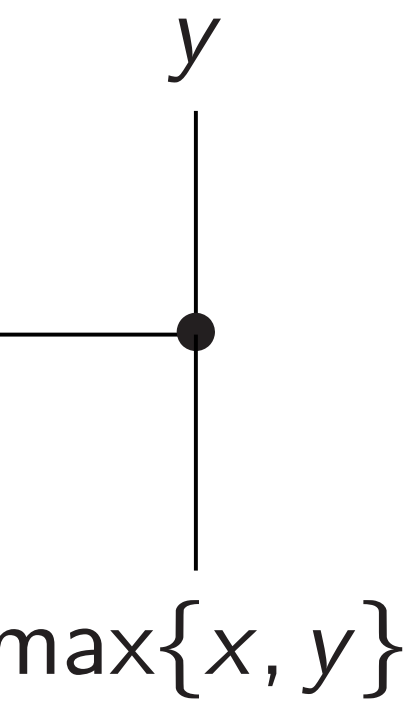
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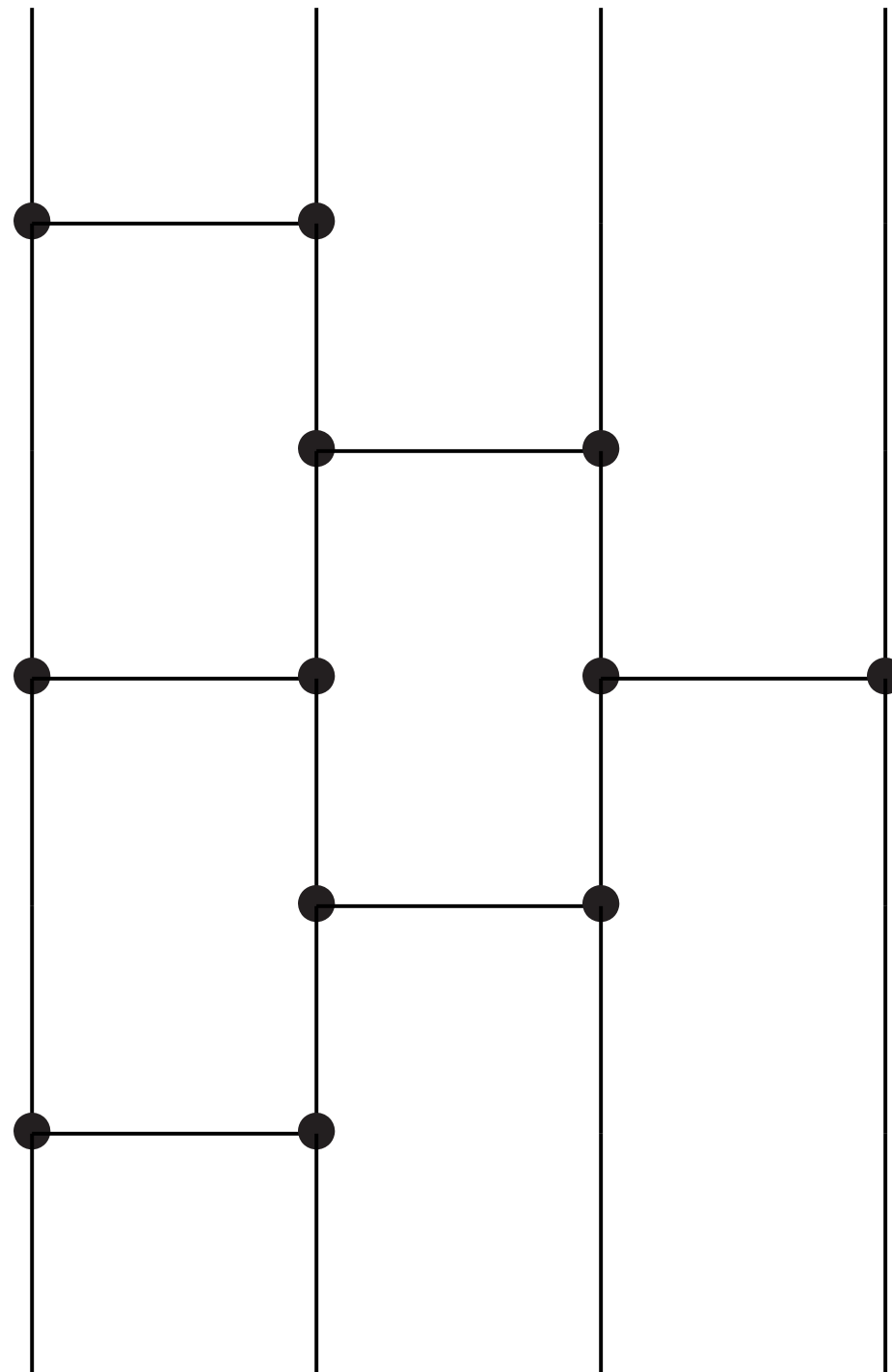
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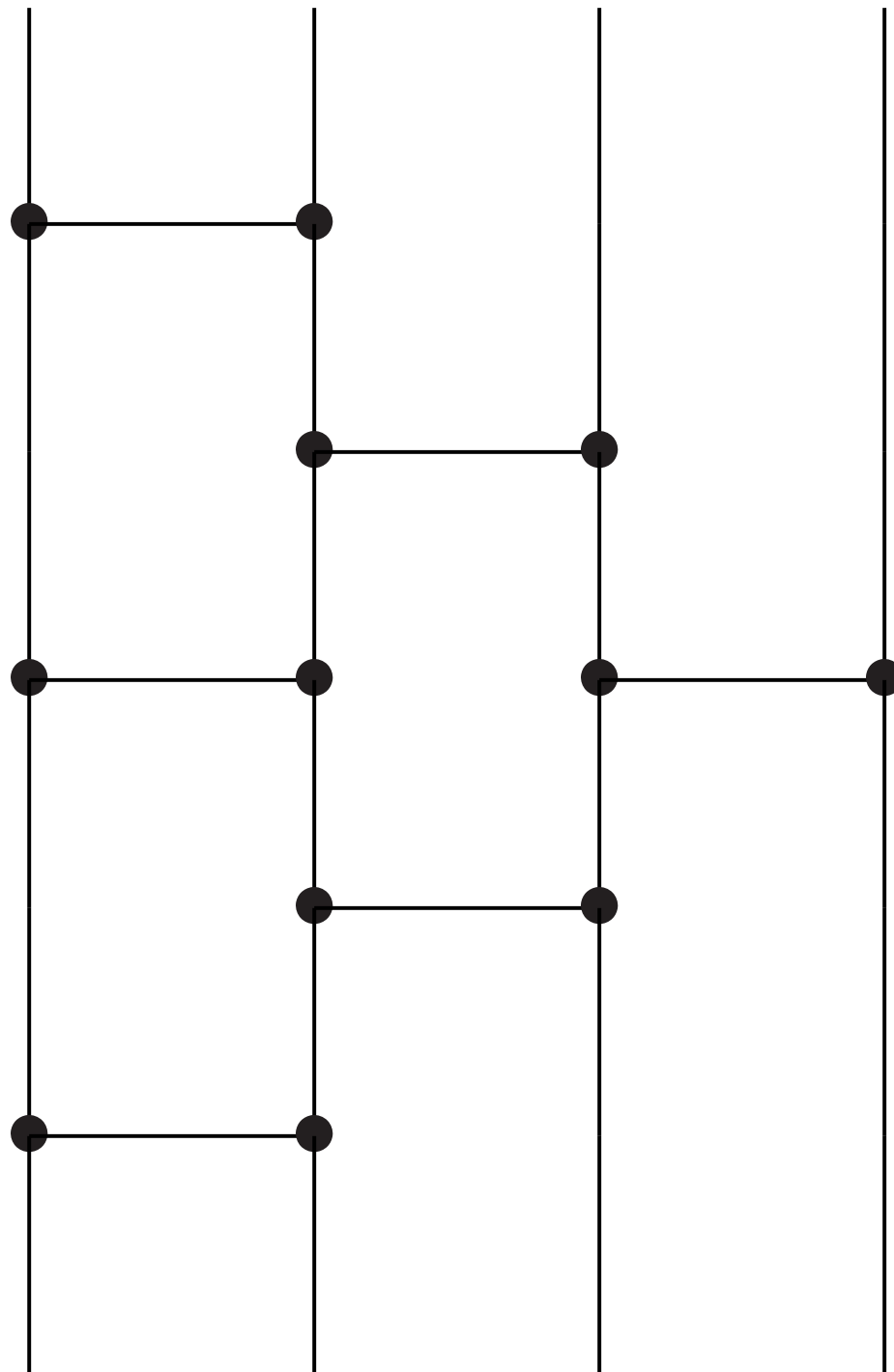


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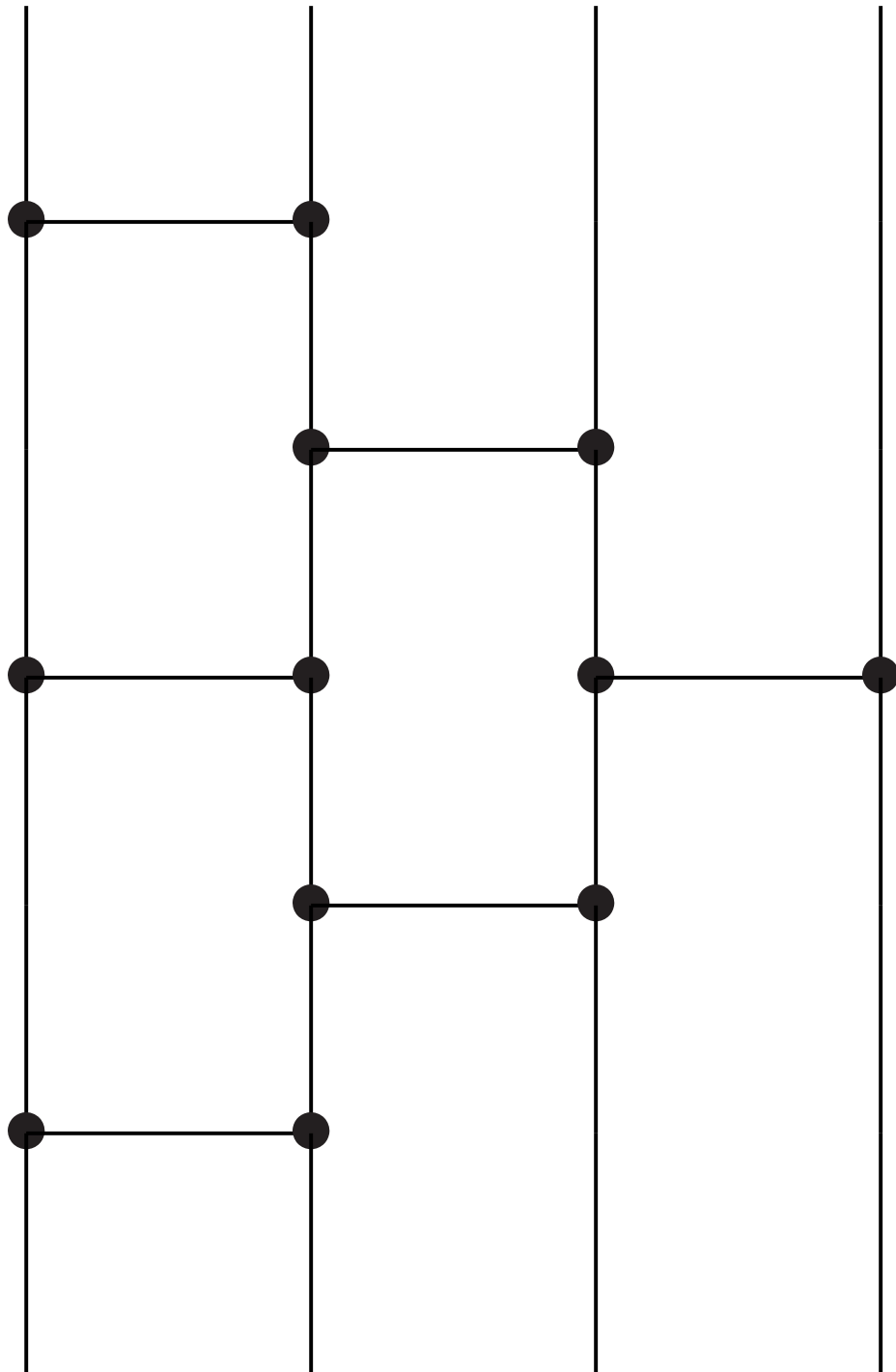
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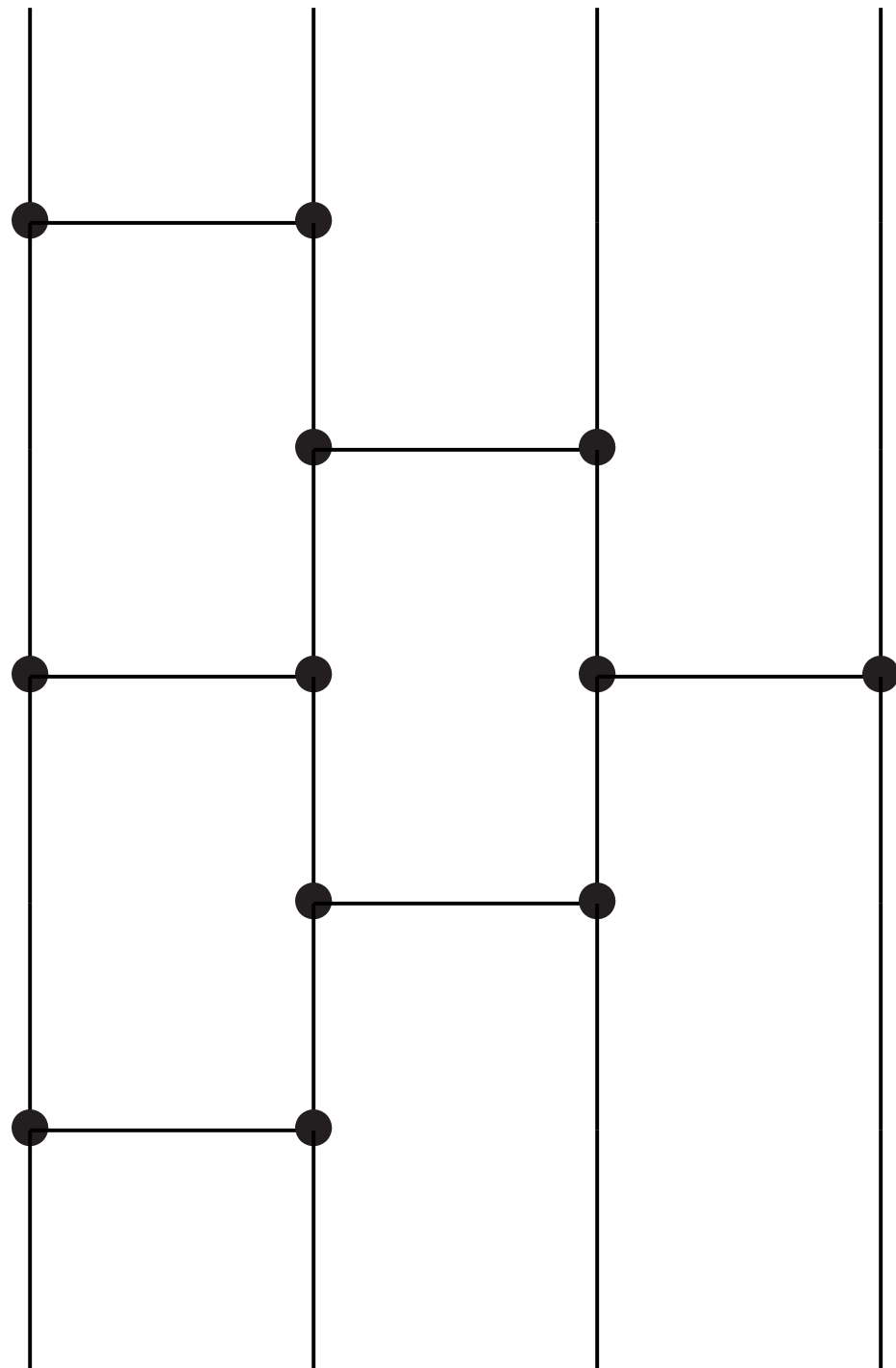
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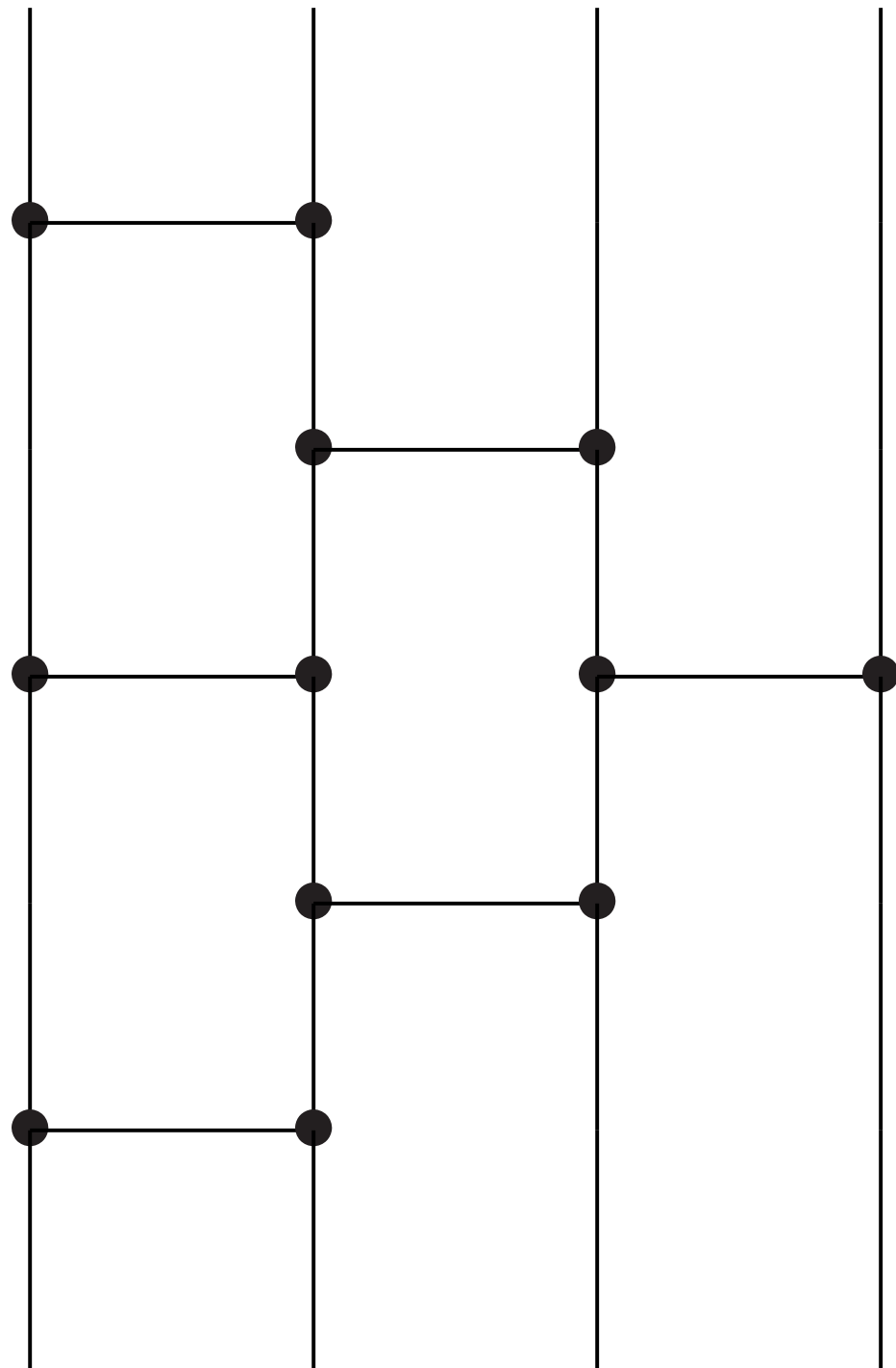


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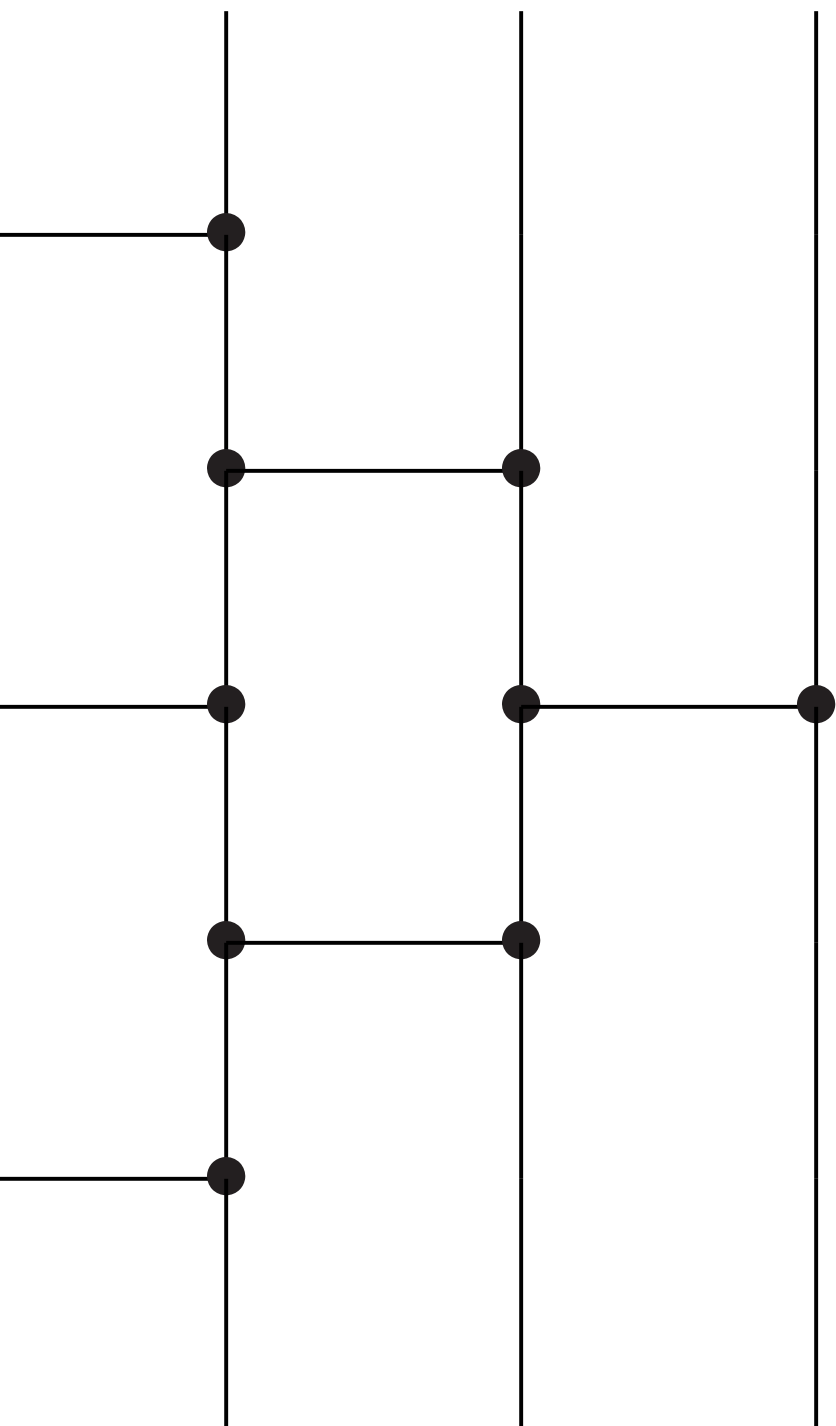
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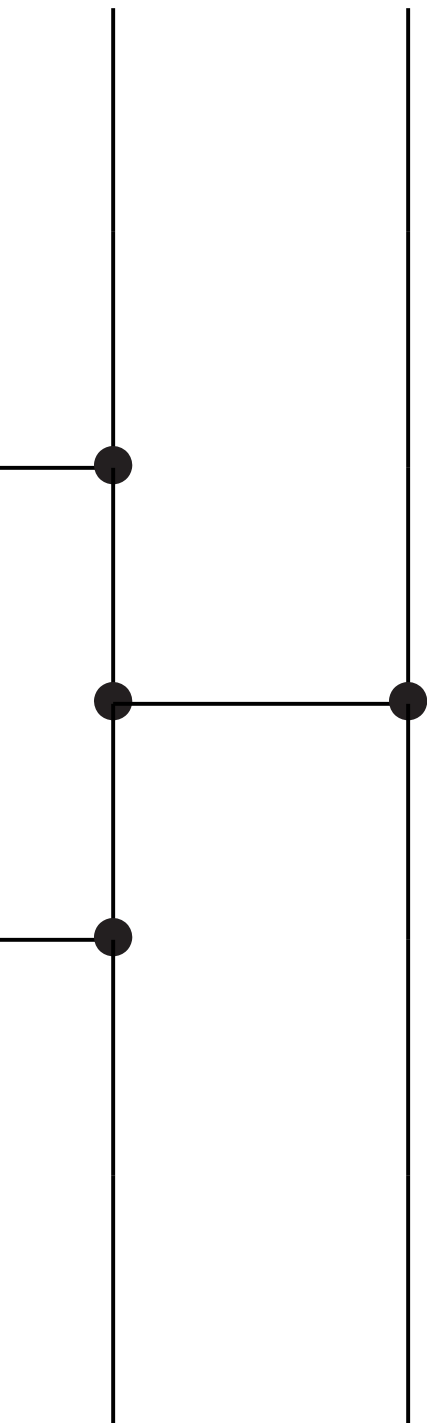
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```
void int
{ int64
  if (n
    t = 1
  while
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  }
}
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void int32_sort(
{ int64 t,p,q,i;
  if (n < 2) ret
  t = 1;
  while (t < n -
  for (p = t;p >
    for (i = 0;i
      if (!(i &
        minmax(x
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    for (i = 0
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    }
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void int32_sort(int32 *x,
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  if (n < 2) return;
  t = 1;
  while (t < n - t) t +=
  for (p = t;p > 0;p >>=
    for (i = 0;i < n - p;
      if (!(i & p))
        minmax(x+i,x+i+p)
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        if (!(i & p))
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i+q);
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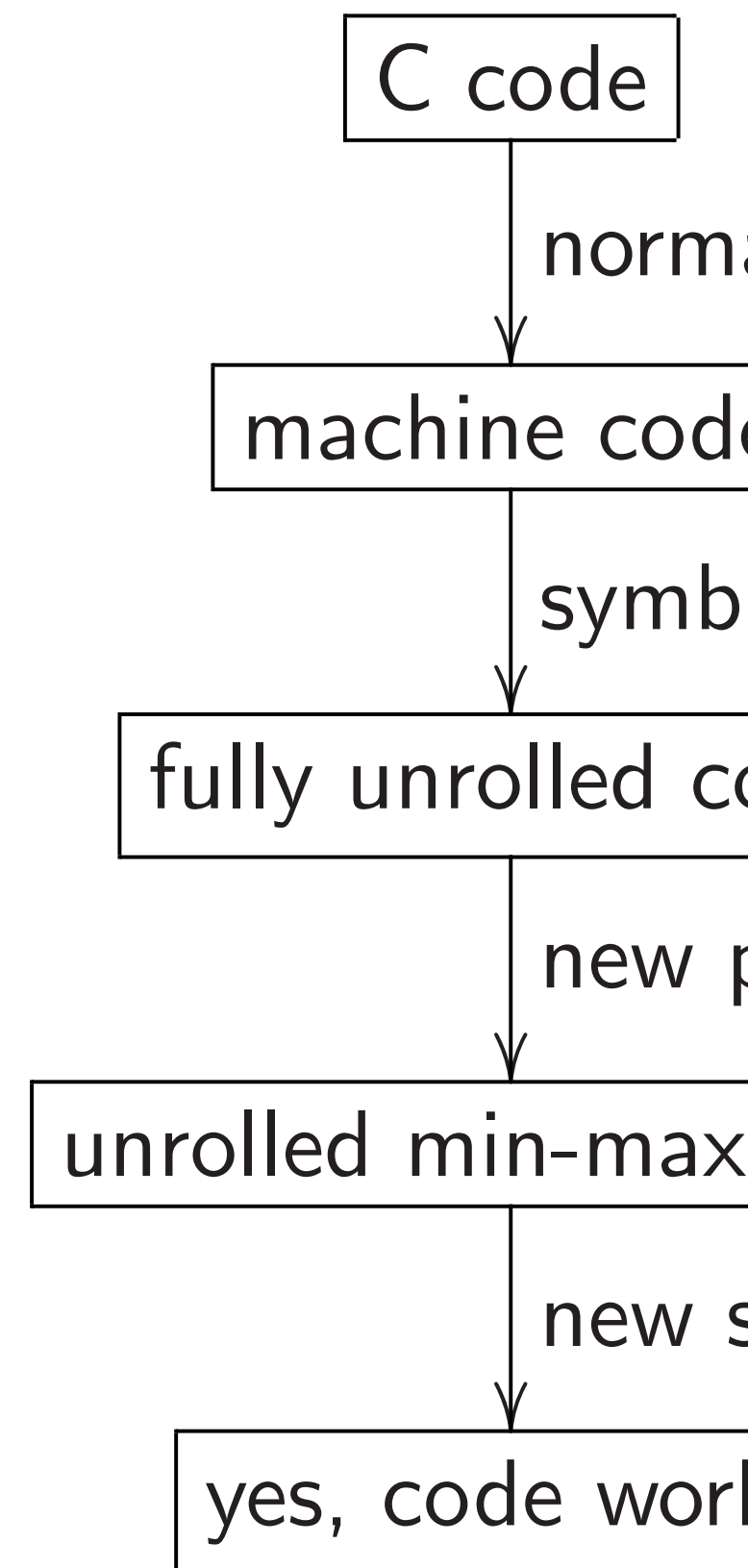
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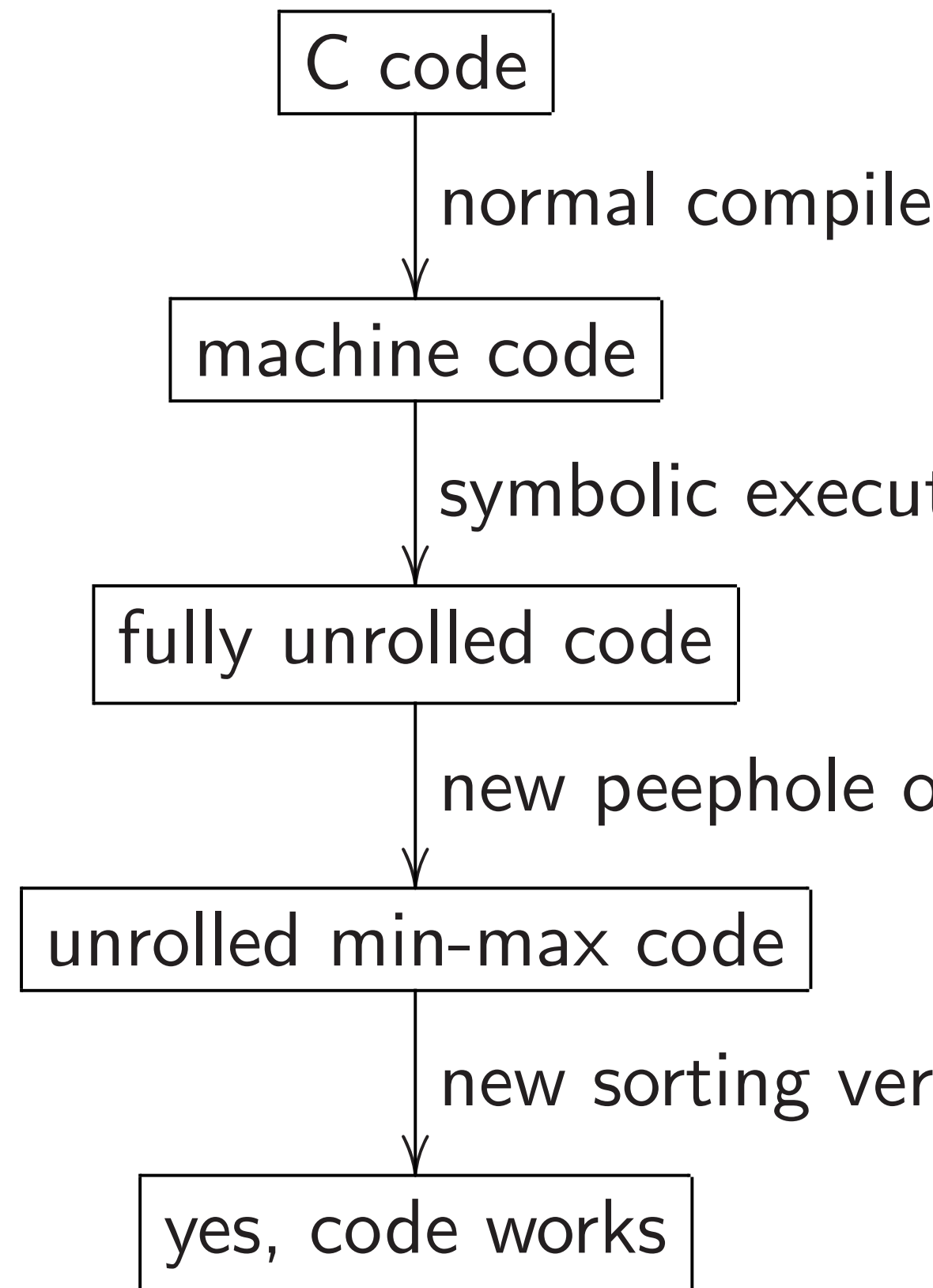
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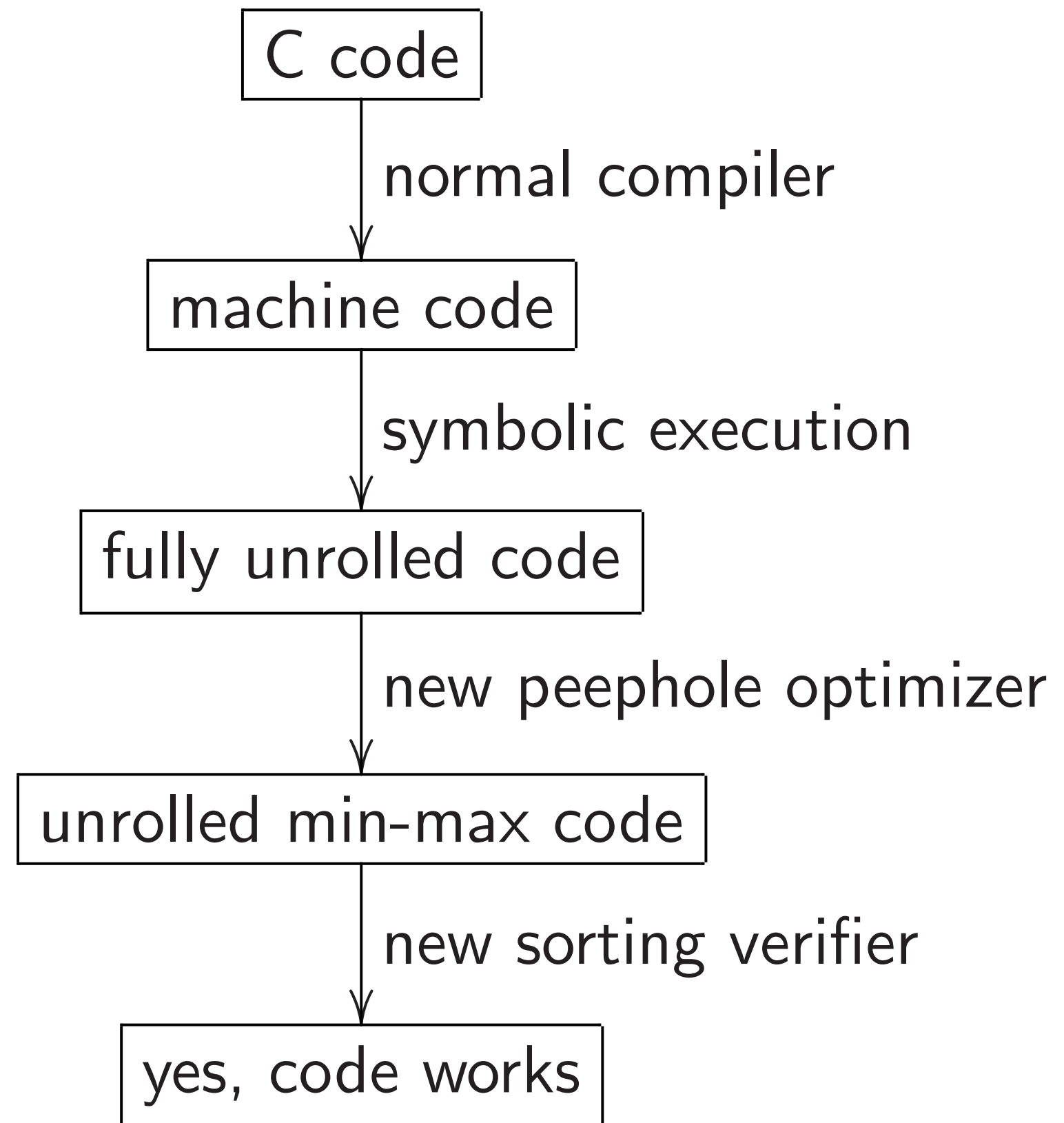
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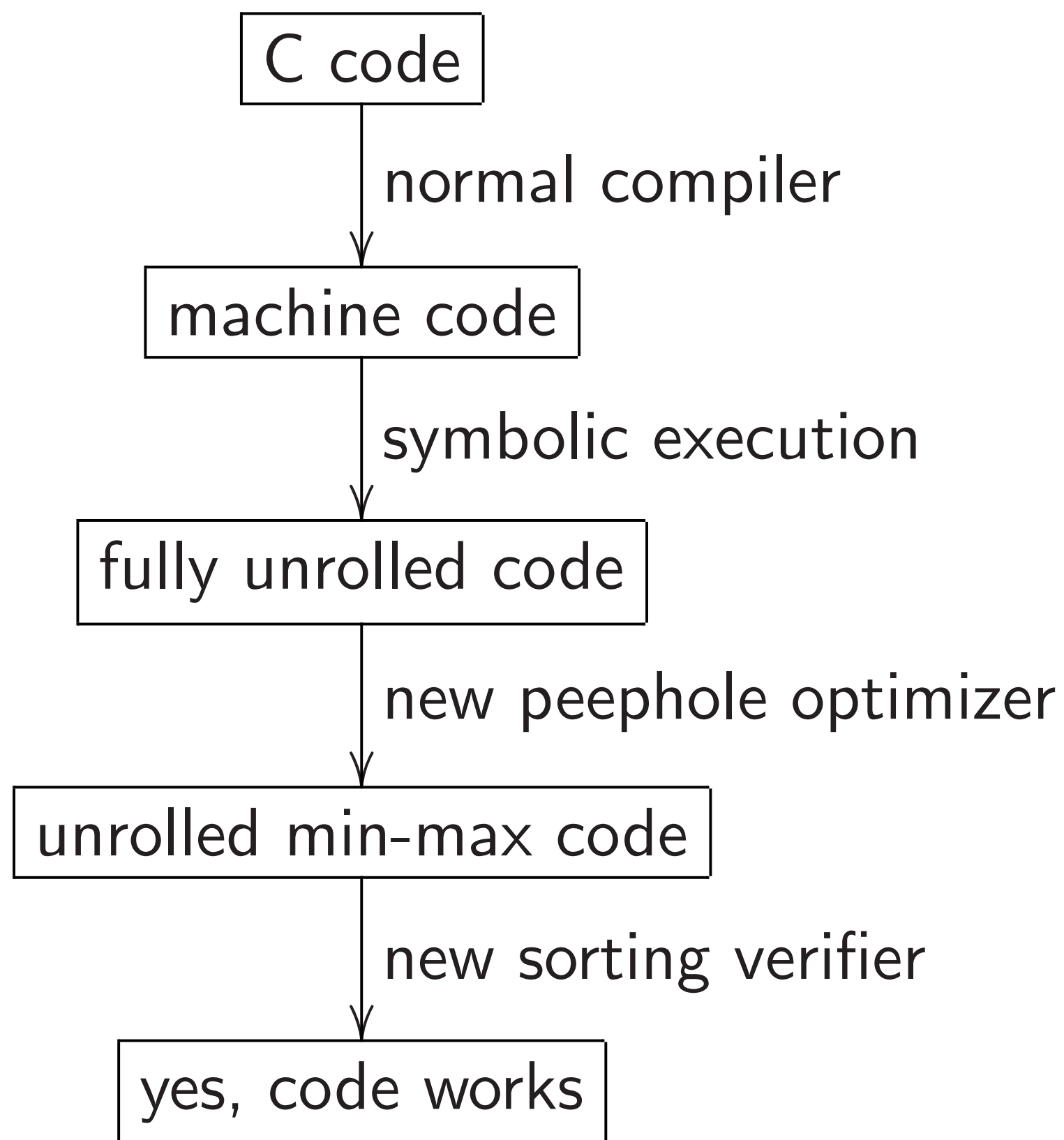
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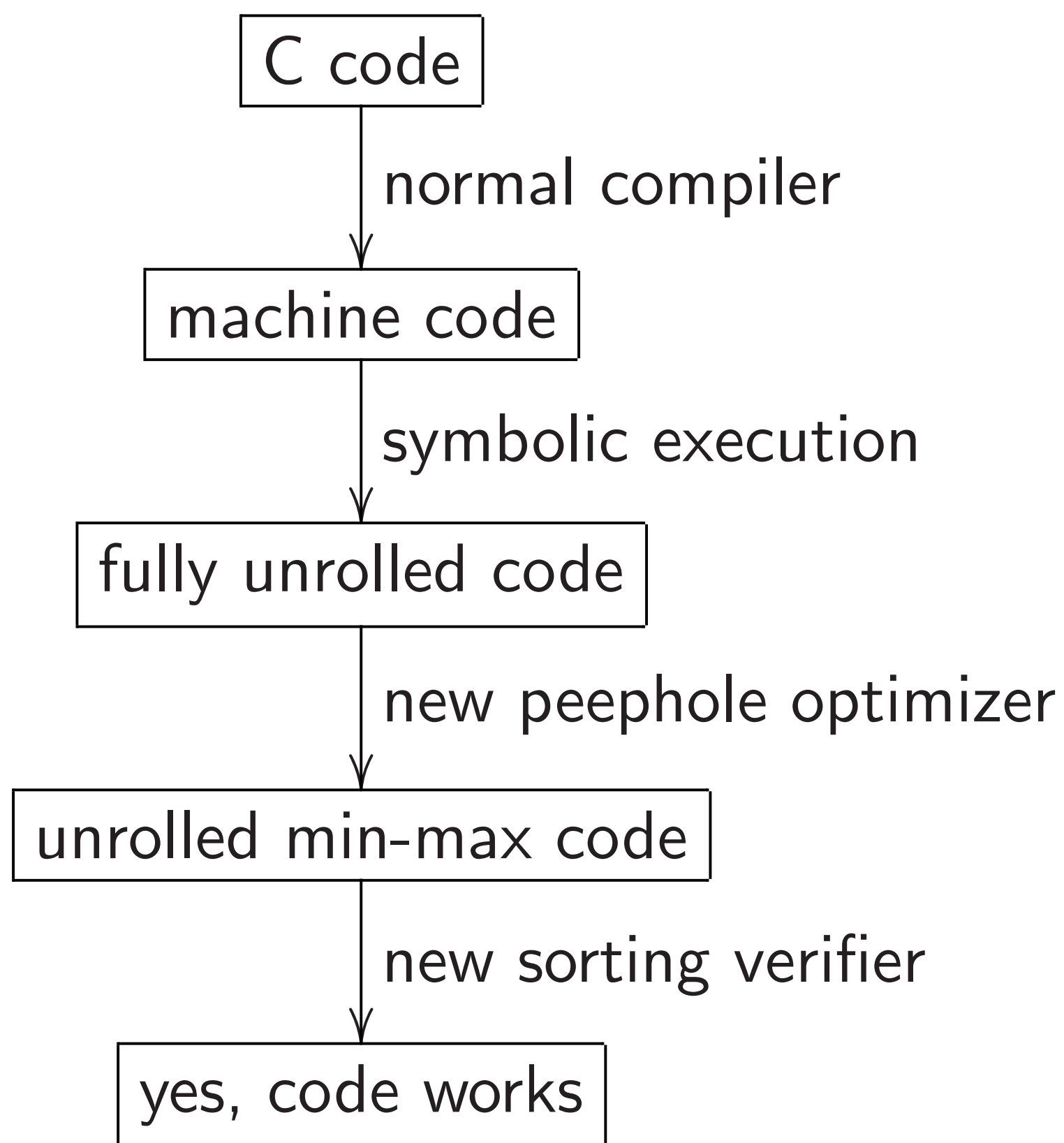
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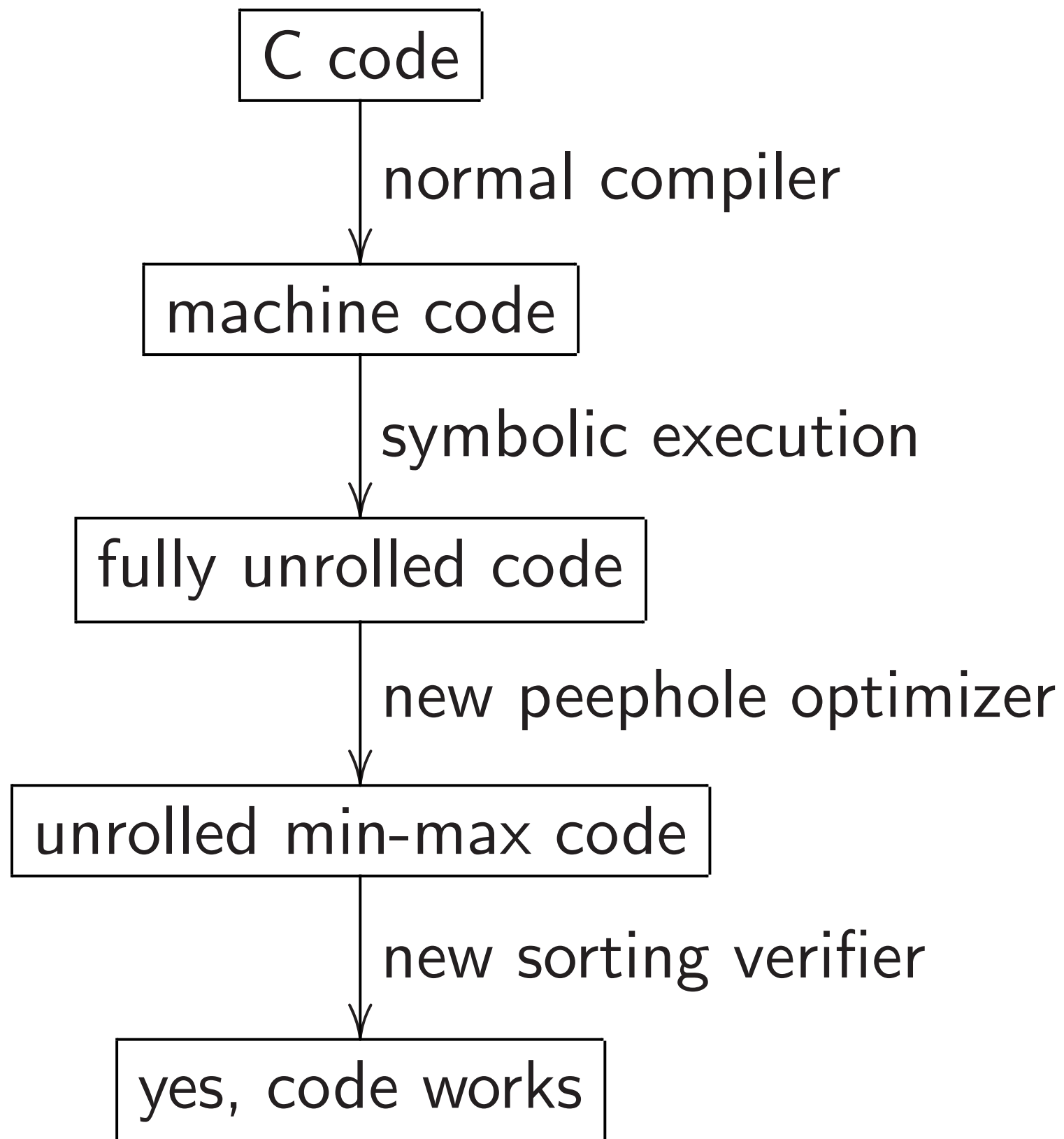
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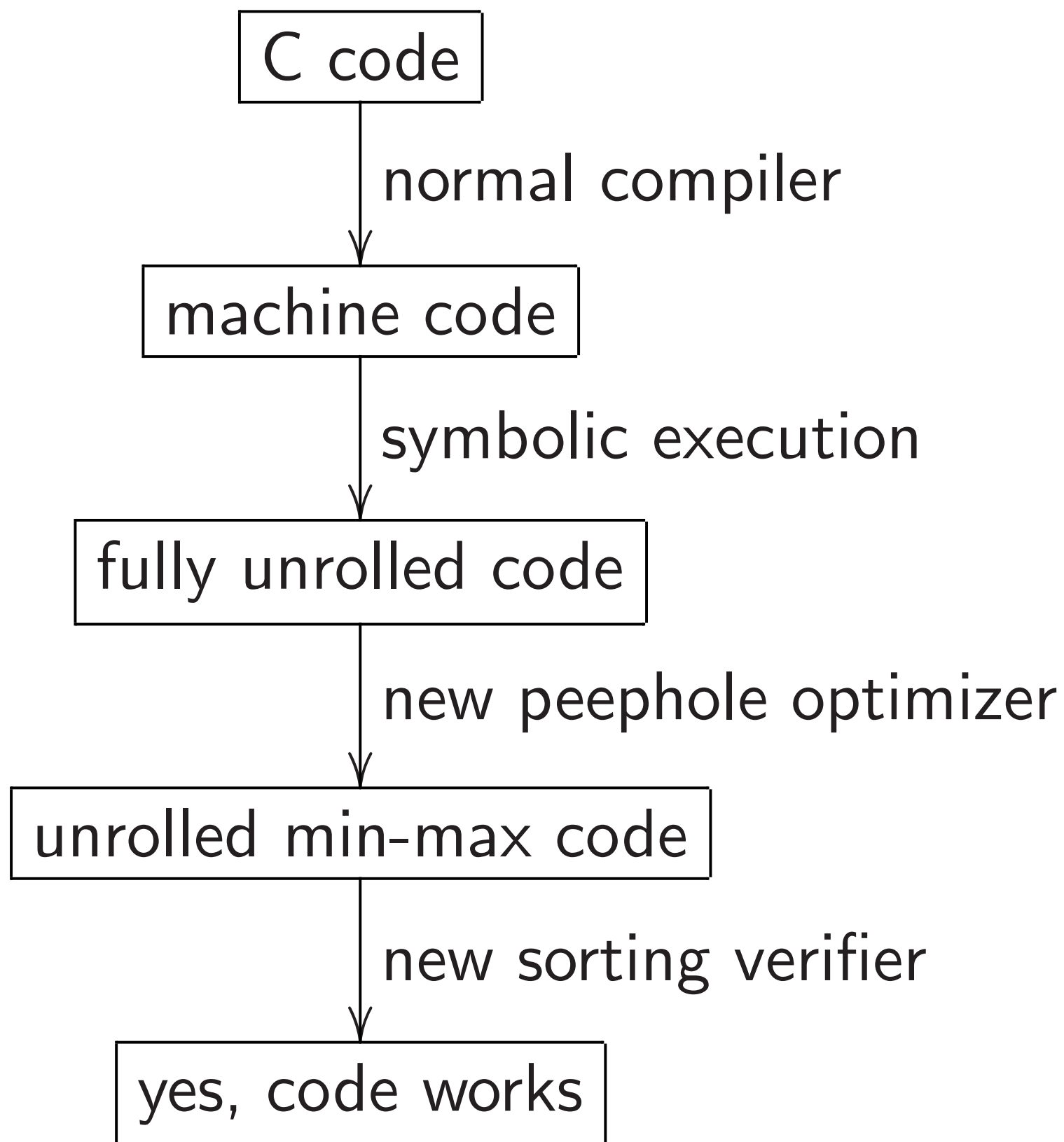
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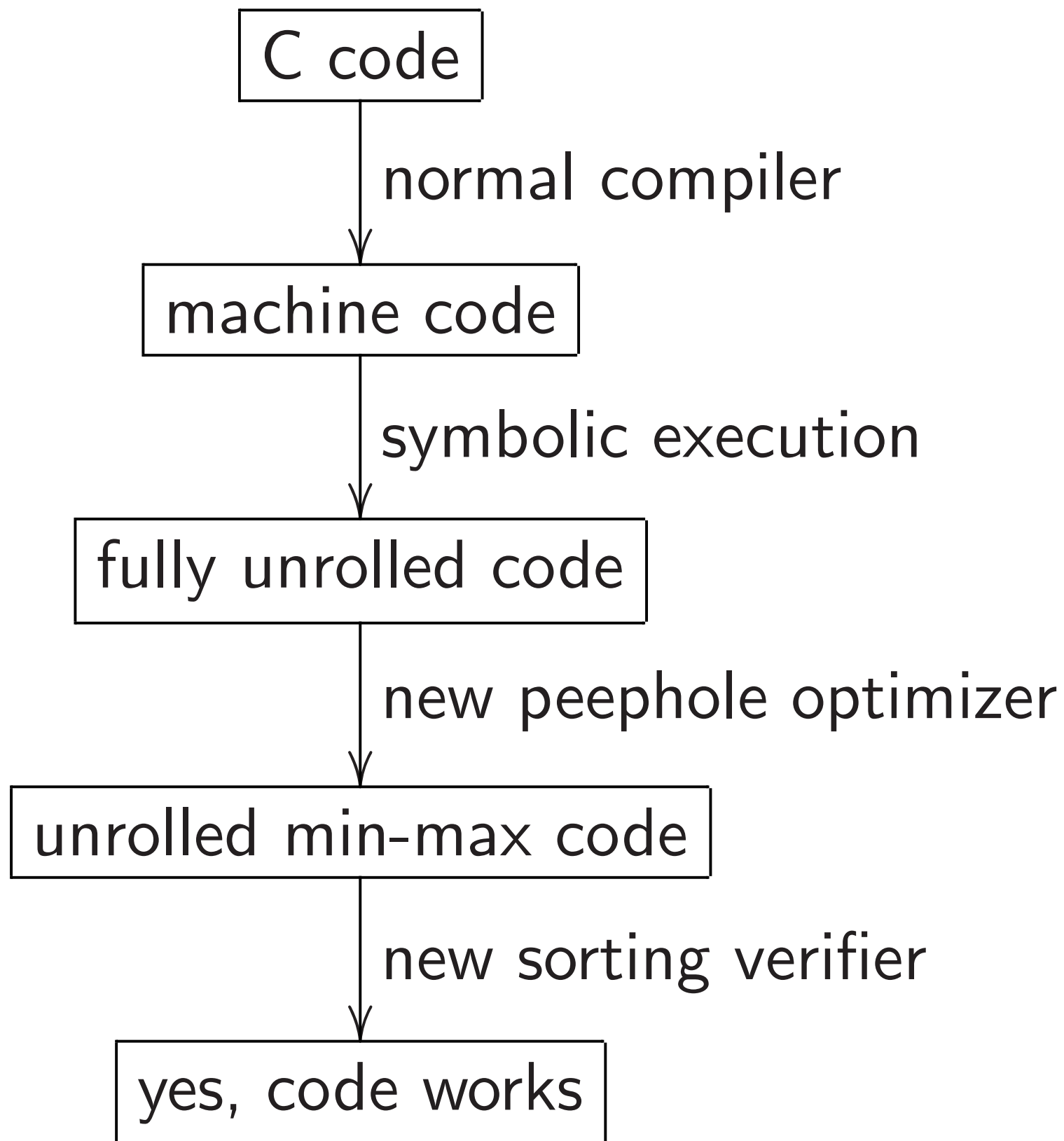
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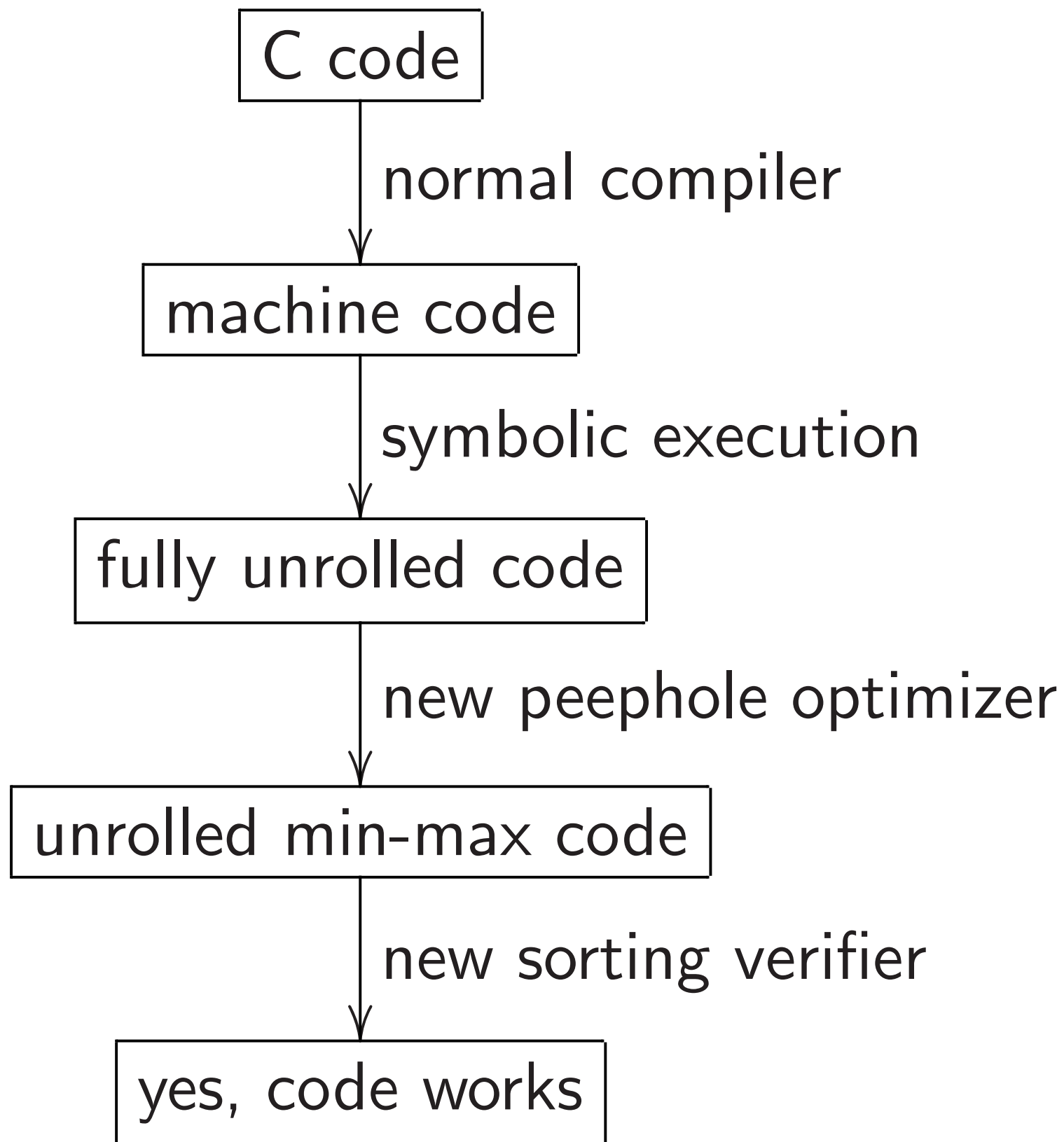
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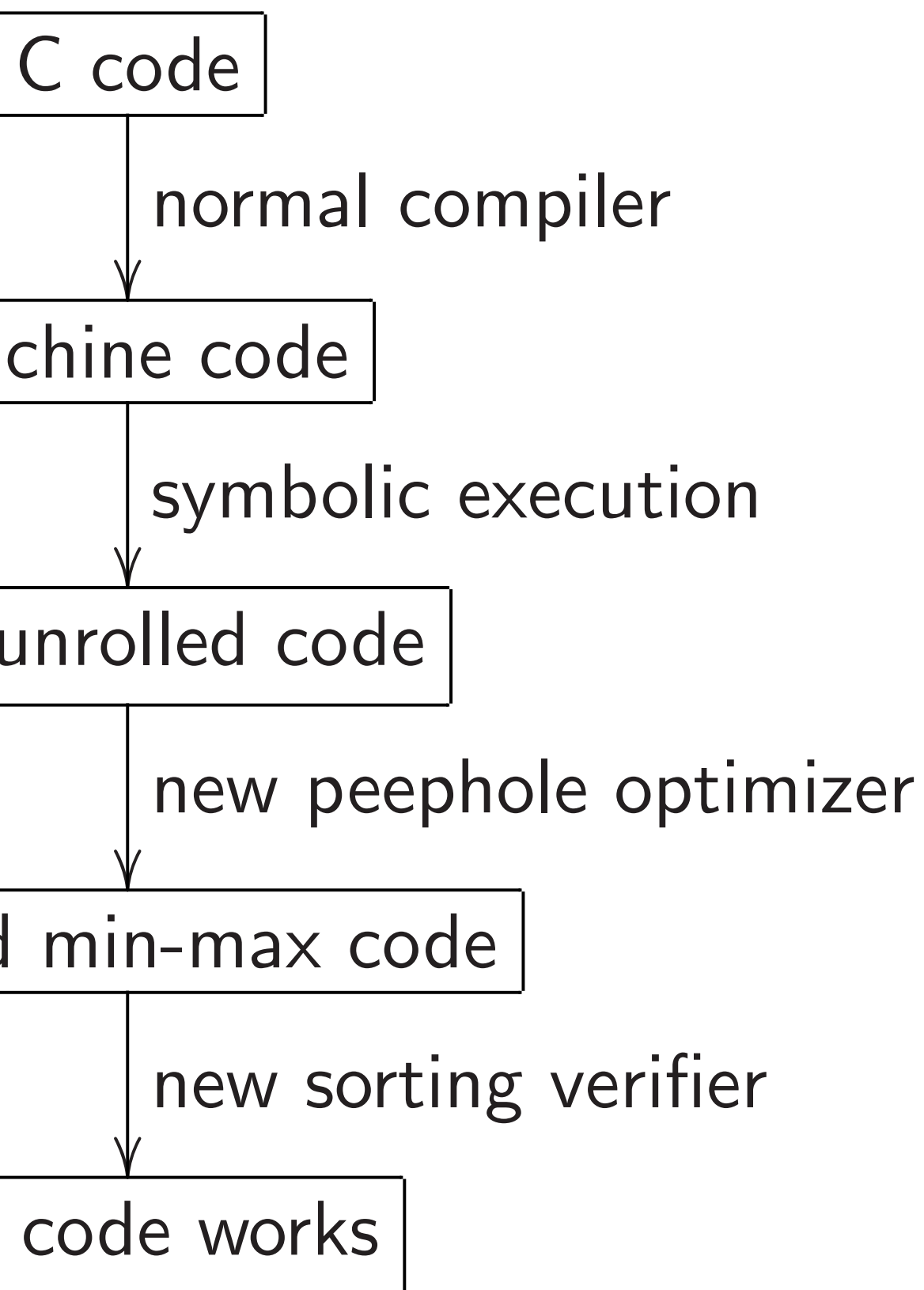
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Sorting verifier: decompose DAG into merging networks.

Verify each merging network using generalization of 2007

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